

FQP10N60C / FQPF10N60C

N-Channel QFET® MOSFET

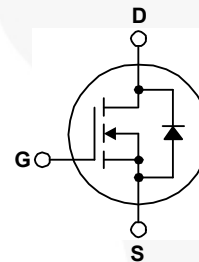
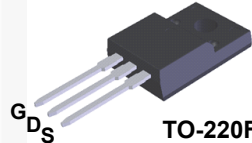
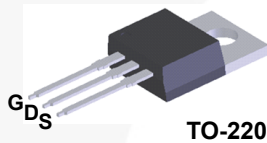
600 V, 9.5 A, 730 mΩ

Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology. This advanced technology has been especially tailored to mini-mize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction, electronic lamp ballasts based on half bridge topology.

Features

- 9.5 A, 600 V, $R_{DS(on)} = 730 \text{ m}\Omega$ (Max.) @ $V_{GS} = 10 \text{ V}$, $I_D = 4.75 \text{ A}$
- Low Gate Charge (Typ. 44 nC)
- Low Crss (Typ. 18 pF)
- 100% Avalanche Tested



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	FQP10N60C	FQPF10N60C	Unit
V_{DSS}	Drain-Source Voltage	600		V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$)	9.5	9.5 *	A
	- Continuous ($T_C = 100^\circ\text{C}$)	5.7	5.7 *	A
I_{DM}	Drain Current - Pulsed (Note 1)	38	38 *	A
V_{GSS}	Gate-Source Voltage	± 30		V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	700		mJ
I_{AR}	Avalanche Current (Note 1)	9.5		A
E_{AR}	Repetitive Avalanche Energy (Note 1)	15.6		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.5		V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	156	50	W
	- Derate above 25°C	1.25	0.4	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150		$^\circ\text{C}$
T_L	Maximum lead temperature for soldering, 1/8" from case for 5 seconds	300		$^\circ\text{C}$

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FQP10N60C	FQPF10N60C	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.8	2.5	$^\circ\text{C}/\text{W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.5	--	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	62.5	$^\circ\text{C}/\text{W}$

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQP10N60C	FQP10N60C	TO-220	Tube	N/A	N/A	50 units
FQPF10N60C	FQPF10N60C	TO-220F	Tube	N/A	N/A	50 units
FQPF10N60CT	FQPF10N60CT	TO-220F	Tube	N/A	N/A	50 units
FQPF10N60C_F105	FQPF10N60C	TO-220F	Tube	N/A	N/A	50 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	600	--	--	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	--	0.7	--	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600 V, V _{GS} = 0 V	--	--	1	μA
		V _{DS} = 480 V, T _C = 125°C	--	--	10	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.0	--	4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 4.75 A	--	0.6	0.73	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 4.75 A	--	8.0	--	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz	--	1570	2040	pF
C _{oss}	Output Capacitance		--	166	215	pF
C _{rss}	Reverse Transfer Capacitance		--	18	24	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 300 V, I _D = 9.5 A, R _G = 25 Ω	--	23	55	ns
t _r	Turn-On Rise Time		--	69	150	ns
t _{d(off)}	Turn-Off Delay Time		--	144	300	ns
t _f	Turn-Off Fall Time		(Note 4)	--	77	165
Q _g	Total Gate Charge	V _{DS} = 480 V, I _D = 9.5 A, V _{GS} = 10 V	--	44	57	nC
Q _{gs}	Gate-Source Charge		--	6.7	--	nC
Q _{gd}	Gate-Drain Charge		(Note 4)	--	18.5	--
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	9.5	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	38	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 9.5 A	--	--	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 9.5 A, dI _F / dt = 100 A/μs	--	420	--	ns
Q _{rr}	Reverse Recovery Charge		--	4.2	--	μC

NOTES:

1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. L = 14.2 mH, I_{AS} = 9.5 A, V_{DD} = 50 V, R_G = 25 Ω, starting T_J = 25°C.
3. I_{SD} ≤ 9.5 A, di/dt ≤ 200 A/μs, V_{DD} ≤ BV_{DSS}, starting T_J = 25°C.
4. Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

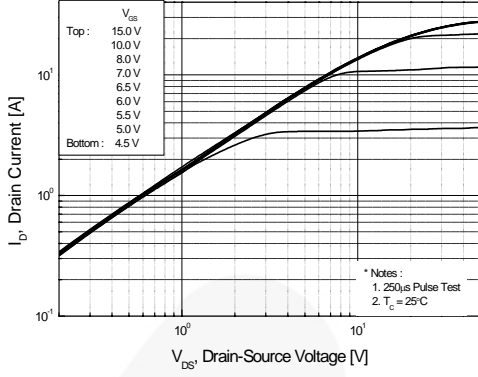


Figure 2. Transfer Characteristics

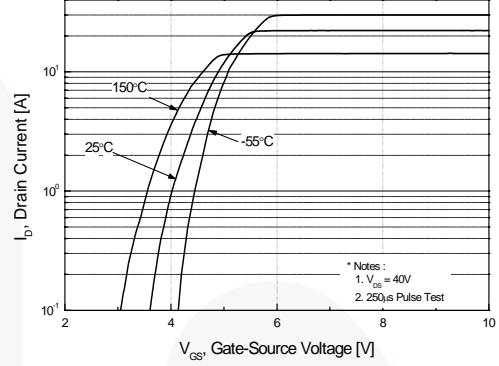


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

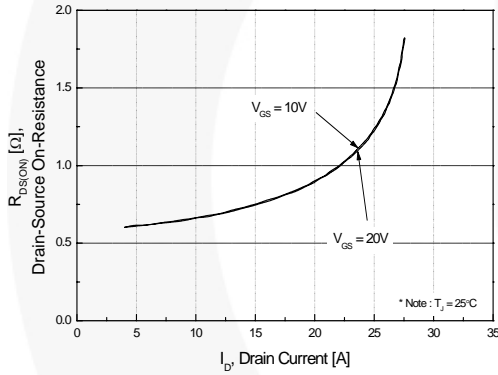


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

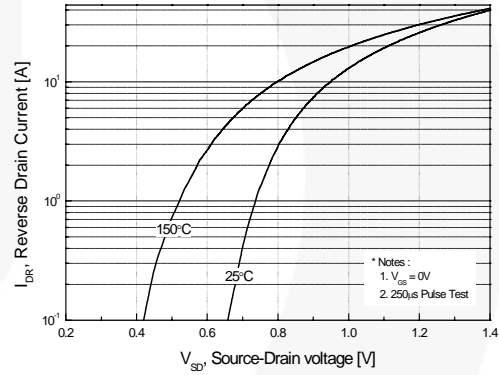


Figure 5. Capacitance Characteristics

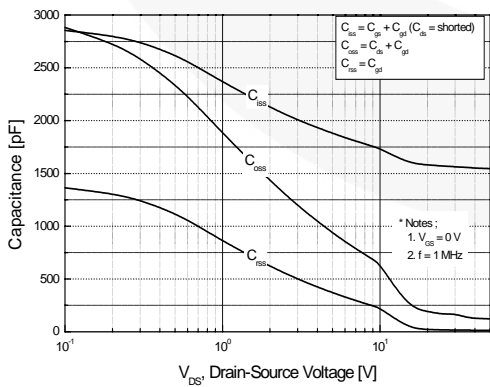
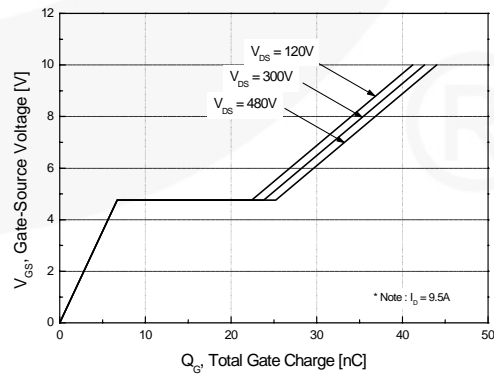


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

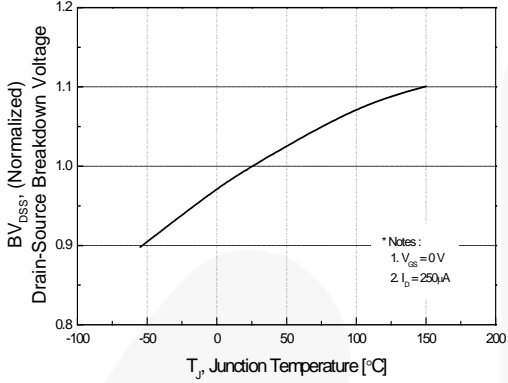


Figure 8. On-Resistance Variation vs. Temperature

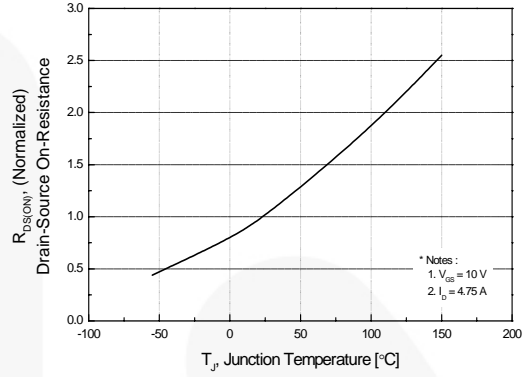


Figure 9-1. Maximum Safe Operating Area for FQP10N60C

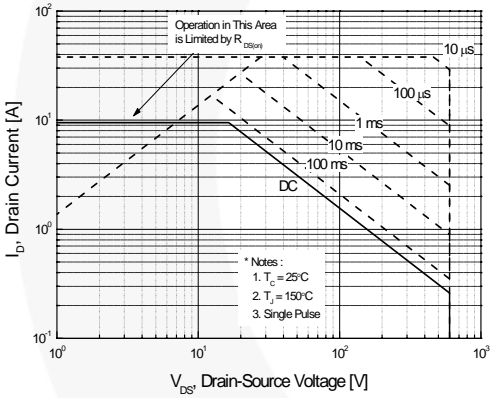


Figure 9-2. Maximum Safe Operating Area for FQPF10N60C

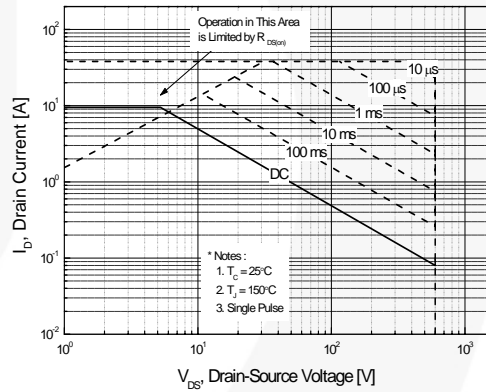
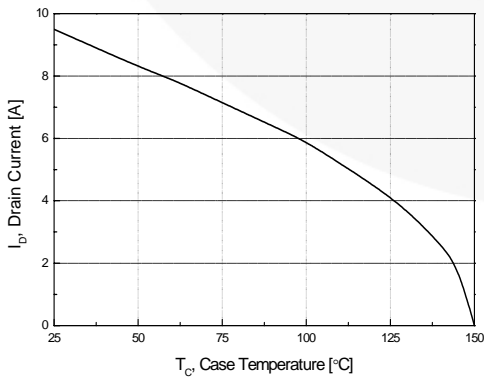


Figure 10. Maximum Drain Current vs. Case Temperature



Typical Performance Characteristics (Continued)

Figure 11-1. Transient Thermal Response Curve for FQP10N60C

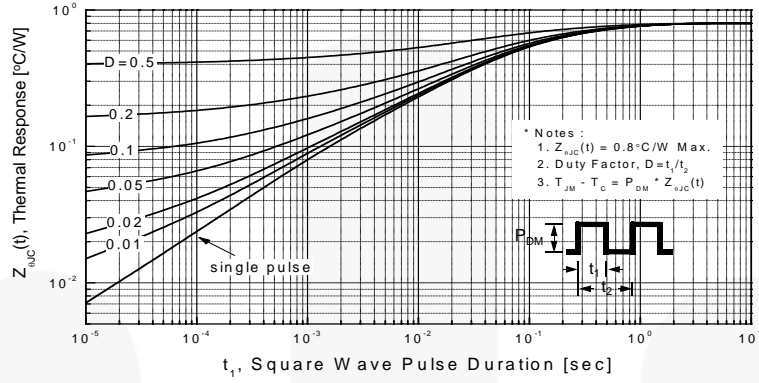
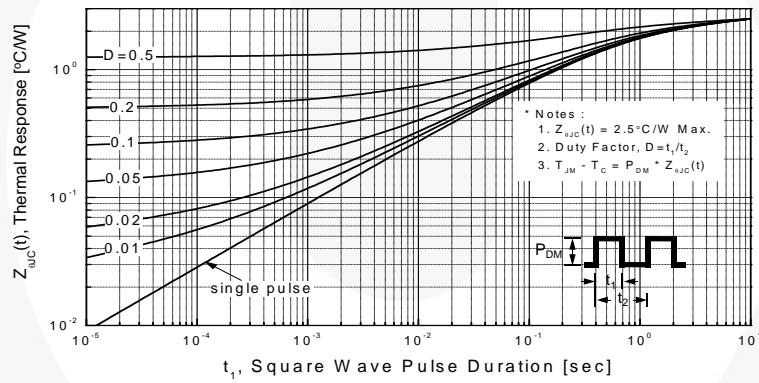


Figure 11-2. Transient Thermal Response Curve for FQPF10N60C



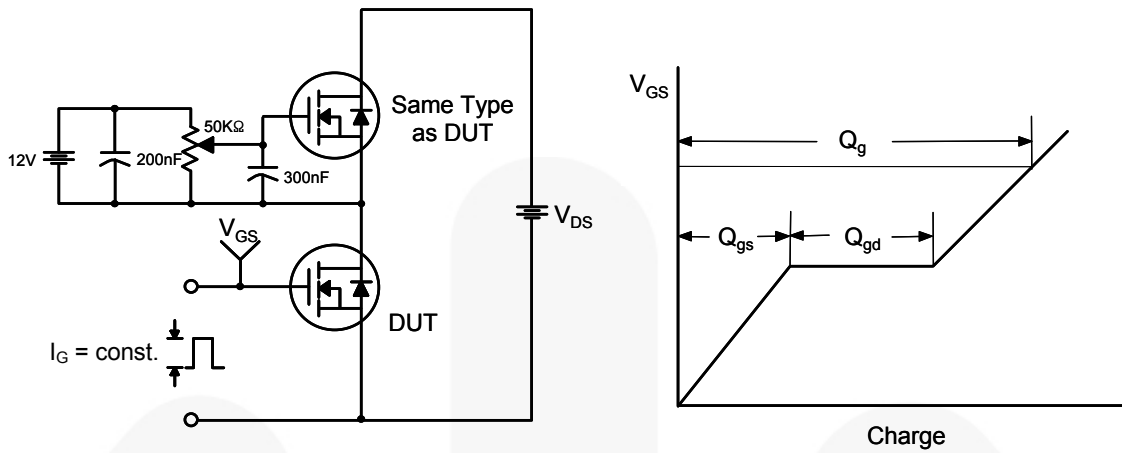


Figure 12. Gate Charge Test Circuit & Waveform

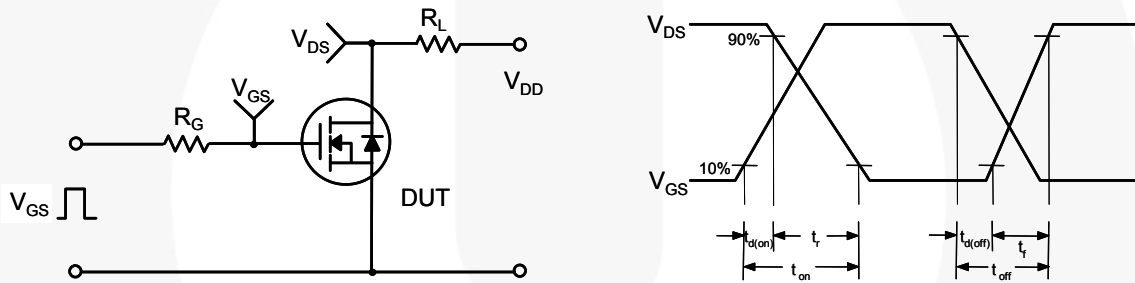


Figure 13. Resistive Switching Test Circuit & Waveforms

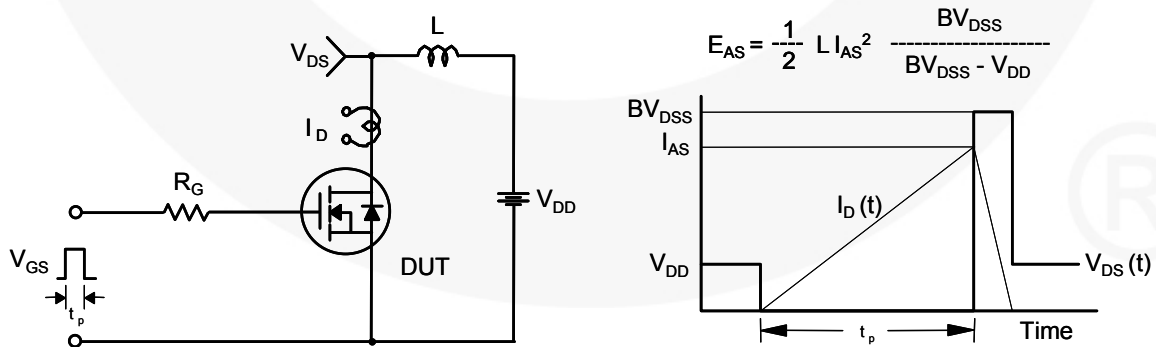


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

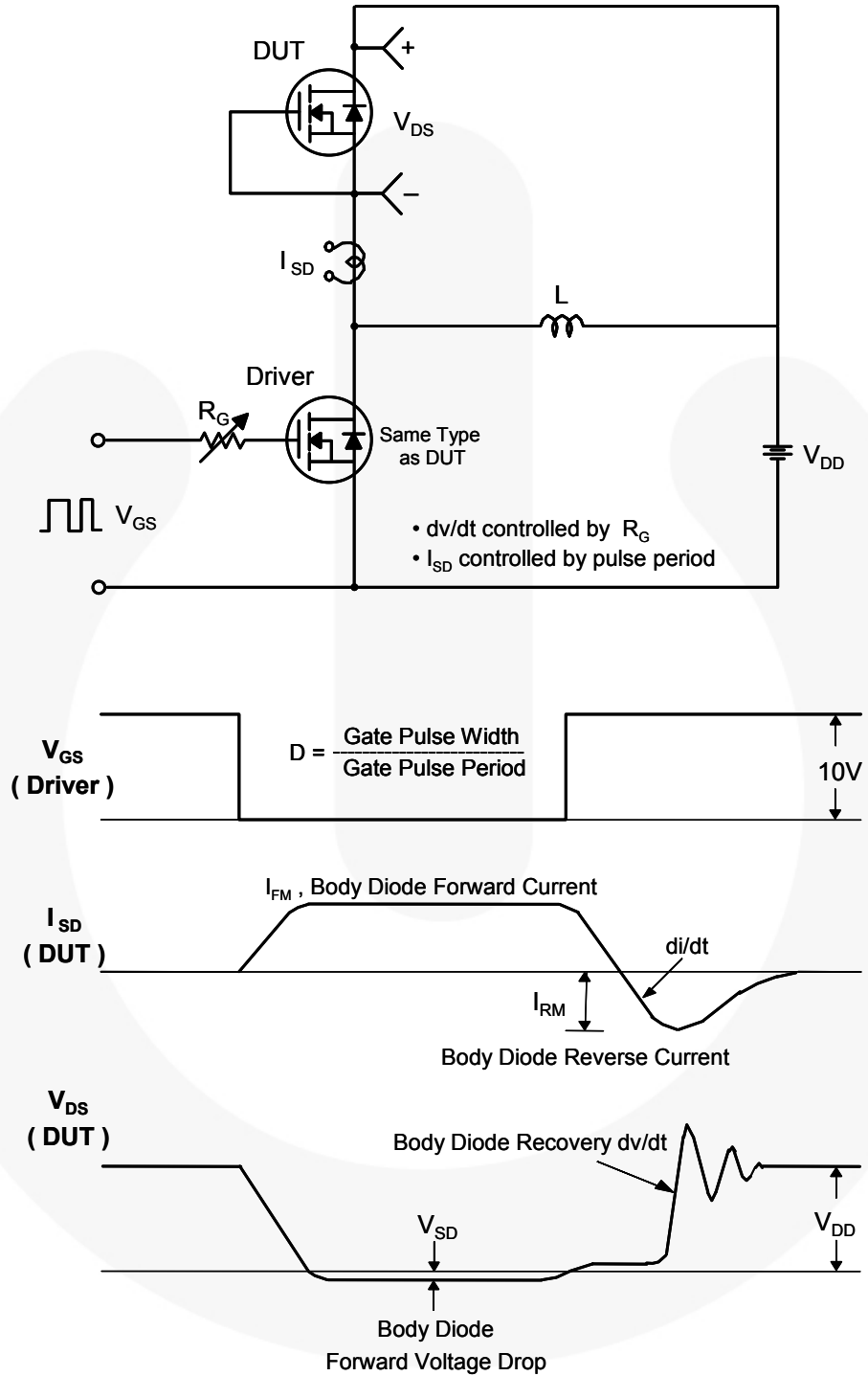


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions



Figure 16. TO-220, Molded, 3-Lead, Jedec Variation AB

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

http://www.fairchildsemi.com/package/packageDetails.html?id=PN_TT220-003

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[Fairchild Semiconductor:](#)

[FQPF10N60CT](#) [FQPF10N60CYDTU](#)