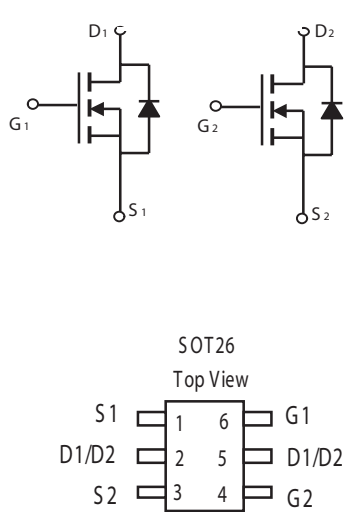


PRODUCT SUMMARY		
$V_{DS}$	$R_{DS(ON)}$ @ 4.0V (typ)	$I_D$
20V	21m $\Omega$	4A

**FEATURES**

- Super high dense cell design for low  $R_{DS(ON)}$ .
- Rugged and reliable.
- Surface Mount Package.



SOT26  
Top View

## ABSOLUTE MAXIMUM RATINGS ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	V
Drain Current-Continuous <sup>a</sup> @ $T_J=25^\circ\text{C}$	$I_D$	4	A
-Pulsed <sup>b</sup>	$I_{DM}$	25	A
Drain-Source Diode Forward Current <sup>a</sup>	$I_S$	1.25	A
Maximum Power Dissipation <sup>a</sup>	PD	1.25	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

## THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient <sup>a</sup>	R $\theta_{JA}$	100	$^\circ\text{C/W}$
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## ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	20			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 19.5V, V <sub>GS</sub> = 0V			1	μA
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> = ±10V, V <sub>DS</sub> = 0V			± 0.1	μA
<b>ON CHARACTERISTICS<sup>b</sup></b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	0.45		1.2	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 4.5A		21	25	m ohm
		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 3.5A			37.5	m ohm
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> = 5.0V, I <sub>D</sub> = 4A		10		S
<b>DYNAMIC CHARACTERISTICS<sup>c</sup></b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 8V, V <sub>GS</sub> = 0V f = 1.0MHz		608		pF
Output Capacitance	C <sub>OSS</sub>			115		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			86		pF
<b>SWITCHING CHARACTERISTICS<sup>c</sup></b>						
Turn-On Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> = 10V, I <sub>D</sub> = 1A, V <sub>GEN</sub> = 4.5V, R <sub>L</sub> = 10 ohm R <sub>GEN</sub> = 10 ohm		10		ns
Rise Time	t <sub>r</sub>			14		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>			39		ns
Fall Time	t <sub>f</sub>			20		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4A, V <sub>GS</sub> = 4.5V		11		nC
		V <sub>DS</sub> = 10V, I <sub>D</sub> = 5A, V <sub>GS</sub> = 2.5V				nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 5 A		2.3		nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> = 4.5V		2.5		nC

ELECTRICAL CHARACTERISTICS ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS <sup>b</sup>						
Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=1.7A$			1.2	V

Notes

- a. Reflow soldering internal actual temperature < 250 degrees, time in high temperature < 7 s.
- b. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
- c. Guaranteed by design, not subject to production testing.

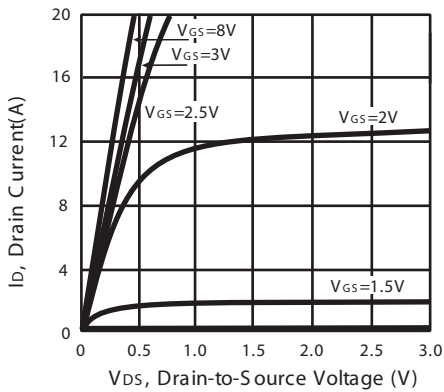


Figure 1. Output Characteristics

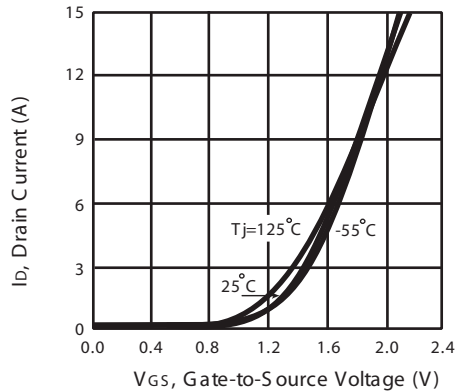


Figure 2. Transfer Characteristics

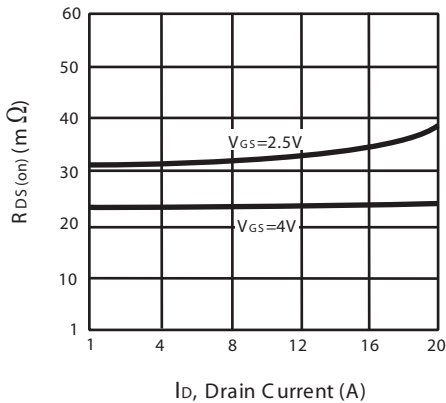


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

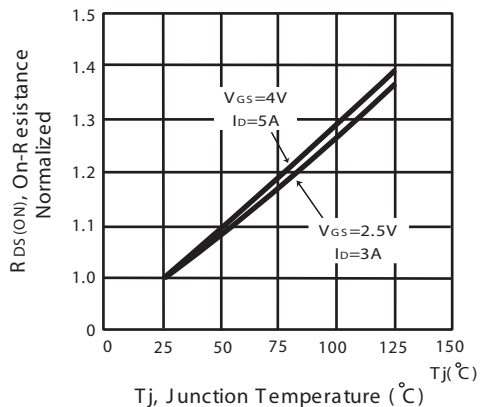


Figure 4. On-Resistance Variation with Drain Current and Temperature

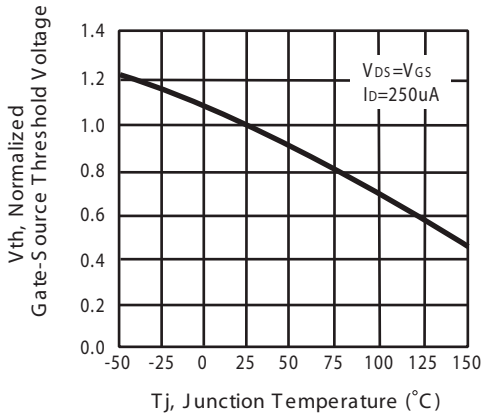


Figure 5. Gate Threshold Variation with Temperature

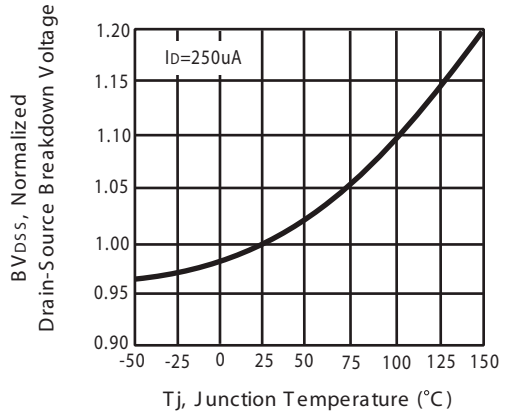


Figure 6. Breakdown Voltage Variation with Temperature

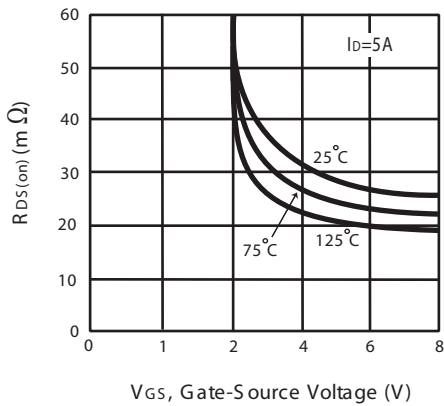


Figure 7. On-Resistance vs. Gate-Source Voltage

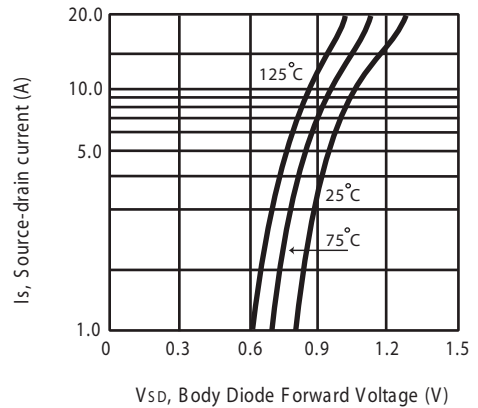


Figure 8. Body Diode Forward Voltage Variation with Source Current

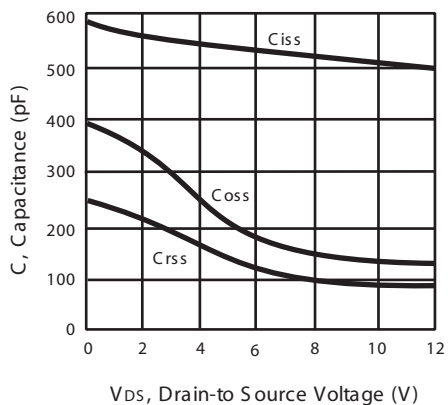


Figure 9. Capacitance

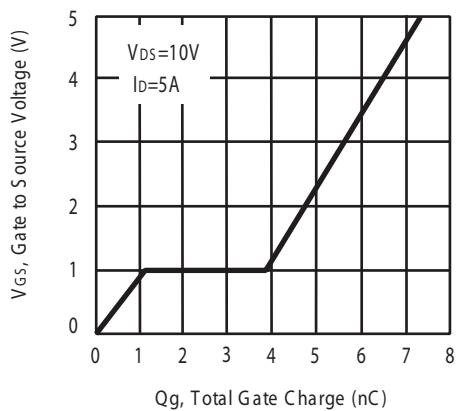


Figure 10. Gate Charge

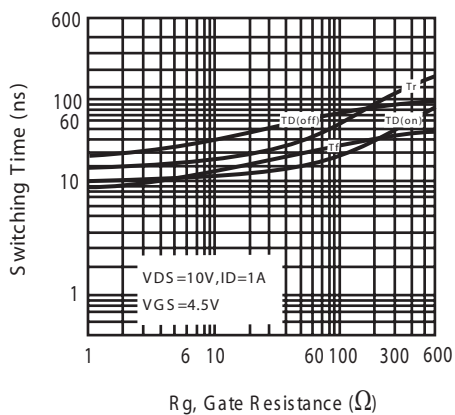
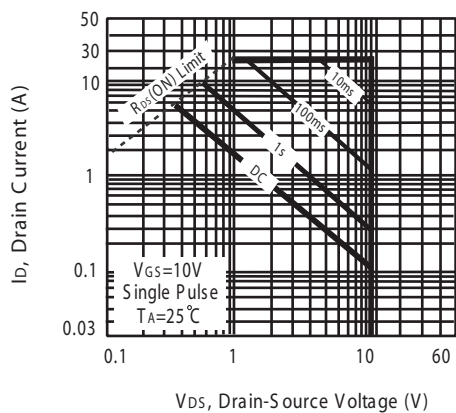


Figure 11. switching characteristics



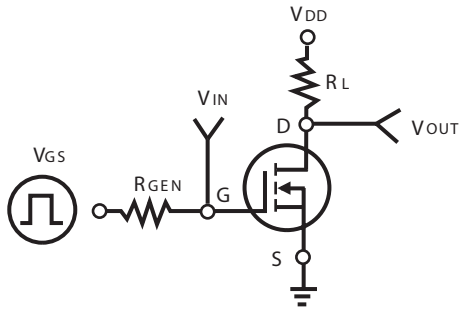


Figure 11. Switching Test Circuit

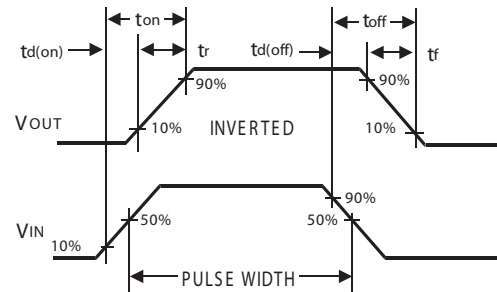


Figure 12. S switching Waveforms

