# KA7552/3

#### **PWM CONTROLLER**

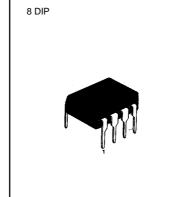
The KA7552/3 are switching power control IC for wide operating frequency range. The internal circuits include pulse by pulse current limiting, protection, on/off control by external trigger, low standby current, soft start, and high current totempole output for driving a POWER MOS-FET.

Maximum duty of the KA7552 is 70% and the KA7553 is 46%. When duty is maximum, the input threshold voltage of pin2 & pin8 are not same in KA7552 and KA7553.

## FEATURES

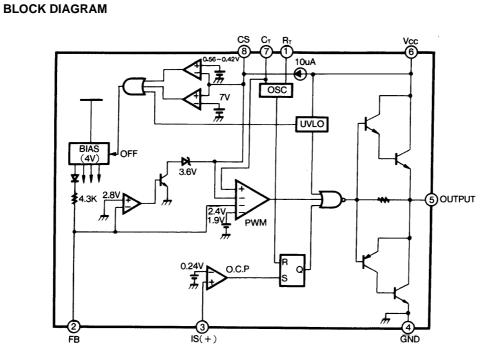
- Built-in Drive Circuits for Direct Connection POWER MOSFET (I<sub>O</sub>= ± 1.5A)
- Wide Operating Frequency Range (5KHz ~ 600KHz)
  Pulse By Pulse Over Current Limiting
- Over Load Protection
- On/Off Control By External Trigger
- Internal UVLO
- Low Standby Current (Typ. 90uA)
- Soft Start Circuit

**SMPS CONTROLLER** 



#### **ORDERING IN FORMATION**

	Device	Package	Operating Temperature
I	KA7552/3	8 DIP	<b>-25 ~ + 85</b> ℃





SEMICONDUCTOR ©1999 Fairchild Semiconductor Corporation

# KA7552/3

# SMPS CONTROLLER

# ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Value	Unit
Supply Voltage	V <sub>CC</sub>	30	V
Output Current	lo	± 1.5	A
Input Voltage at Overcurrent Detection Pin	V <sub>IN(IS)</sub>	- 0.3 to 4	V
Input Voltage at FB Pin	V <sub>IN(FB)</sub>	4	V
Input Current at CS Pin	I <sub>IN(CS)</sub>	2	mA
Total Power Dissipation(Ta = 25℃)	PD	800	mW
Operating Temperature	T <sub>OPR</sub>	- 25 to 85	C

### ELECTRICAL CHARACTERISTICS

(V\_{CC} = 18V,  $F_{OSC}$  = 135KHz,  $T_{A}$  = 25  $^{\circ}\!\!\!\mathrm{C}$  , unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Тур	Max	Unit	
OSCILLATOR SECTION	OSCILLATOR SECTION						
Initial Accuracy	Fosc	$C_T$ = 360pF, $T_J$ = 25 $^\circ$ C	125	135	145	KHz	
Frequency Variation 1	Δ F/Δ V	$V_{CC} = 10V$ to 30V	-	± 1	± 3	%	
Frequency Variation 2*	Δ F/Δ V	T <sub>A</sub> = 25℃ to 85℃	-	± 1.5	-	%	
Ramp High Voltage	V <sub>RH</sub>	С⊤ = 360рF, Т <sub>ј</sub> ј= 25 °С	2.80	3.08	3.30	V	
Ramp Low Voltage	V <sub>RL</sub>	$C_T$ = 360pF, $T_J$ = 25 °C	0.6	0.9	1.2	V	
Amplitude	Vosc	V <sub>PIN7</sub> , Peak to Peak	1.80	2.18	2.50	V	
PULSE WIDTH MODULATION S	PULSE WIDTH MODULATION SECTION						
Input Threshold Voltage(Pin2)	V <sub>TH(FBD)</sub>	Duty Cycle = 0%	0.6	0.75	0.95	V	
Input Threshold Voltage(Pin2)*	V <sub>TH(FB1)</sub> (KA7552)	Duty Cycle = Dmax 1	2.1	2.3	2.6	V	
input rifeshold voltage(rifiz)	V <sub>TH(FB2)</sub> (KA7553)	Duty Cycle = Dmax 2	1.6	1.8	2.1	V	
Mary Duty Oralia	D <sub>(Max 1)</sub> (KA7552)	-	66	70	74	%	
Max. Duty Cycle	D <sub>(Max 2)</sub> (KA7553)	-	43	46	49	%	
Source Current(Pin2)	I <sub>SOURCE(FB)</sub>	$V_{PIN2} = 0V$	- 660	- 800	- 960	uA	



# **SMPS CONTROLLER**

# ELECTRICAL CHARACTERISTICS(Continued)

(V\_{CC} = 18V, F\_{OSC} = 135Khz,  $T_{A}$  = 25  $^{\circ}\mathrm{C}$  , unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Тур	Max	Unit
OVERCURRENT LIMIT SECTION						
Input Threshold Voltage	V <sub>TH(IS)</sub>	-	0.21	0.24	0.27	V
Source Current(Pin3)	I <sub>SOURCE(IS)</sub>	VPIN3 = 0V	-300	-200	-100	uA
Deley Time <sup>*</sup>	T <sub>D</sub>	-	-	150	-	ns
SOFT START SECTION						
Charging Current	I <sub>CHG</sub>	$V_{PIN8} = 0V$	-15	-10	-5	uA
Input Threshold Voltage(Pin8)	V <sub>TH(CSO)</sub>	Duty Cycle = Dmax 1	0.7	0.9	1.1	V
Input Threshold Voltage(Pin8)*	V <sub>TH(CS1)</sub> (KA7552)	Duty Cycle = Dmax 2	2.2	2.4	2.6	V
	V <sub>TH(CS2)</sub> (KA7553)		1.7	1.9	2.1	V
LATCH MODE SHUTDOWN CIRC	UIT SECTION					
Sink Current(Pin8)	ISINK(CS)	$V_{\text{PIN8}} = 6V, V_{\text{PIN2}} = 1V$	25	45	65	uA
Shutdown Threshold Voltage	V <sub>TH(SD,CS)</sub>	-	6.7	7.2	7.7	V
OVERLOAD SHUTDOWN SECTION	N					
Shudown Threshold Voltage	V <sub>TH(SD,FB)</sub>	=	2.6	2.8	3.1	V
UNDER VOLTAGE LOCKOUT SE	CTION					
Start-Up Threshold Voltage	V <sub>TH(ST)</sub>	-	15.5	16.0	16.5	V
Minimum Operating Voltage	V <sub>OPR(Min)</sub>	-	8.20	8.70	9.20	V
Hysteresis	V <sub>HYS</sub>	-	6.40	7.30	8.20	V
ON/OFF CONTROL SECTION	-		•	•	•	
Source Current(Pin8)	I <sub>SOURCE(CS)</sub>	V <sub>PIN8</sub> = 0V	- 15	- 10	- 5	uA
On Threshold Voltage	V <sub>TH(ON)</sub>	V <sub>PIN8</sub> : OFF->ON	0.45	0.56	0.70	V
Off Threshold Voltage	V <sub>TH(OFF)</sub>	V <sub>PIN8</sub> : ON -> OFF	0.30	0.42	0.55	V



# **SMPS CONTROLLER**

## ELECTRICAL CHARACTERISTICS(Continued)

(V\_{CC} = 18V, F\_{OSC} = 135KHz,  $T_A$  = 25  $^\circ\!\!\!\mathrm{C}$  , unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Тур	Max	Unit
OUTPUT SECTION	•	·	•			
Low Output Voltage	V <sub>OL</sub>	I <sub>O</sub> = 100mA, V <sub>CC</sub> = 18V	-	1.3	1.8	V
High Output Voltage	V <sub>OH</sub>	I <sub>O</sub> = -100mA, V <sub>CC</sub> = 18V	16.0	16.5	18.0	V
Rise Time *	T <sub>R</sub>	NO LOAD	-	50	=	ns
Fall Time *	T <sub>F</sub>	NO LOAD	-	50	-	ns
OVERALL						
Stard-by Current	I <sub>SB</sub>	$V_{CC} = 14V$	-	90	150	uA
Operating Current	I <sub>CC(OPR)</sub>	$V_{PIN2} = 0V$	-	9	15	mA
Power Supply Current off	I <sub>CC(OFF)</sub>	$V_{PIN8} = 0V$	-	1.1	1.8	mA
Power Supply Current Shutdown I <sub>CC(SD)</sub>		V <sub>PIN8</sub> = 7.6V	-	1.1	1.8	mA

\* These parameters, although guaranteed, are not 100% tested in production.

NOTE : Recommend Operating Condition  $R_T = 3.3 K_{\odot}^{\circ} \sim 10 K_{\odot}^{\circ}$ , Oscillation Frequency = 5KHz ~ 600KHz Soft Start Condensor(CS) = 0.1uF ~ 1uF



#### TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx<sup>TM</sup> CoolFET<sup>TM</sup> CROSSVOLT<sup>TM</sup> E<sup>2</sup>CMOS<sup>TM</sup> FACT<sup>TM</sup> FACT Quiet Series<sup>TM</sup> FAST<sup>®</sup> FAST<sup>®</sup> FASTr<sup>TM</sup> GTO<sup>TM</sup> HiSeC<sup>TM</sup> ISOPLANAR<sup>™</sup> MICROWIRE<sup>™</sup> POP<sup>™</sup> PowerTrench<sup>™</sup> QS<sup>™</sup> Quiet Series<sup>™</sup> SuperSOT<sup>™</sup>-3 SuperSOT<sup>™</sup>-6 SuperSOT<sup>™</sup>-8 TinyLogic<sup>™</sup> UHC<sup>™</sup> VCX<sup>™</sup>

# DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user. 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.