

SANYO Semiconductors DATA SHEET

LA4225

Monolithic Linear IC

Audio Output for TV application 5W Monaural Power Amplifier

Overview

LA4225 is a 5W monaural power amplifier intended for television audio output.

This IC requires only two external components (capacitors) to construct amplifiers and is ideal for realizing substantial cost reduction of electronic devices.

Functions

- 5W monaural power amplifier (VCC = 18V, $R_L = 8\Omega$)
- Full complement of protection circuits

 Thermal shutdown protector on chip

 Short between an output and DC protection circuit
- On-chip pop noise reduction circuit

Maximum Ratings at $Ta = 25^{\circ}C$

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Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max	Rg = 0	24	V
Maximum output current	I _O peak		3.3	Α
Allowable power dissipation	Pd max	Arbitrarily large heat sink	7.5	W
Operating temperature	Topr		-25 to +75	°C
Storage temperature	Tstg // //		-40 to +150	°C

Operating Conditions at Ta=25°C

Parameter	Symbol Conditions	Ratings	unit
Recommended supply voltage	Vce	13.2	V
Recommended load resistance	RL	4	Ω
Allowable operating voltage range	V _{CC} op Not exceeding the package Pd.	5 to 22	V
Recommended load resistance range	R _L op	4 to 8	Ω

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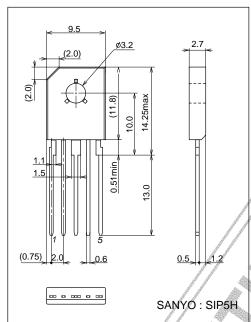
LA4225

Electrical Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 13.2V$, $R_L = 4\Omega$, f = 1 kHz, $Rg = 600\Omega$, Designated substrate and circuit

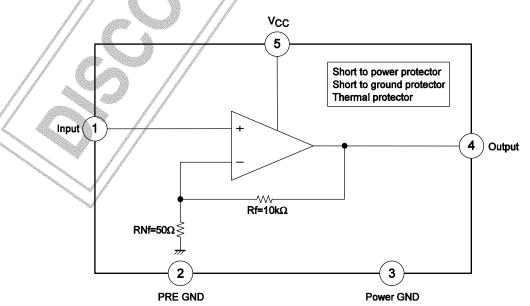
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Quiescent current	Icco	Rg = 0		65	130	mA
Voltage gain	VG	$V_O = 0$ dBm	43	45	47	dB
Output power	P _O 1	$V_{CC} = 13.2V, R_L = 4\Omega, THD = 10\%$	4	5		W
	P _O 2	$V_{CC} = 18V, R_L = 8\Omega, THD = 10\%$		5		W
Total harmonic distortion	THD	P _O = 1W		0.1	1.0	%
Output noise voltage	V _{NO}	Rg = 0, DIN AUDIO		0.15	0.5	mV
Ripple rejection	SVRR1	$Rg = 0$, $f_R = 100Hz$, $V_r = 0dBm$, DIN AUDIO	30	40		dB
	SVRR2	$Rg = 0$, $f_R = 1$ kHz, $V_r = 0$ dBm, DIN AUDIO		47		dB
Input resistance	Ri		A STATE OF THE STA	50		kΩ

Package Dimensions

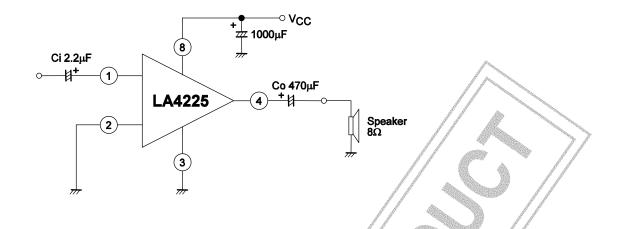
unit : mm 3031C



Block Diagram



Application Circuit Example



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