



SANYO Semiconductors

DATA SHEET

LA4627 — Monolithic Linear IC 2-Channel AF Power Amplifier

Overview

The LA4627 is a 2-channel power amplifier developed for use in radio/cassette player products. The LA4627 reduces the number of required external components by 50% over earlier products (BS/NF capacitors and oscillation prevention RC components) and thus can contribute significantly to space saving in end products.

Features

- Provided in the DIP12F.
- $P_O : 2.0W \times 2$ ($V_{CC} = 9V$, $R_L = 4\Omega$)
 $2.5W \times 2$ ($V_{CC} = 9V$, $R_L = 3\Omega$)
- Standby function built in (supports direct microcontroller control).
- Built-in thermal protection circuit.

Specifications

Maximum Ratings at $T_a = 25^\circ C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V_{CC} max	$R_g = 0$	22	V
Allowable power dissipation	P_d max	When mounted on the Sanyo-recommended PCB	4.0	W
Operating temperature	T_{opr}		-25 to +75	°C
Storage temperature	T_{stg}		-55 to +150	°C

Operating Conditions at $T_a = 25^\circ C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V_{CC}		9	V
Recommended load resistance	R_L		3	Ω
Operating voltage range	V_{CC} op	Under conditions such that the maximum ratings are not exceeded.	5.0 to 20	V
Recommended operating load resistance	R_L op		2.7 to 8.0	Ω

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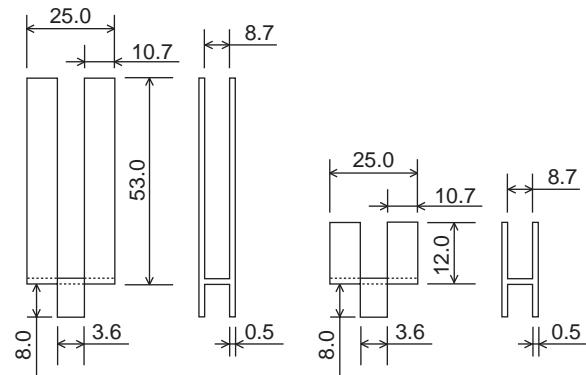
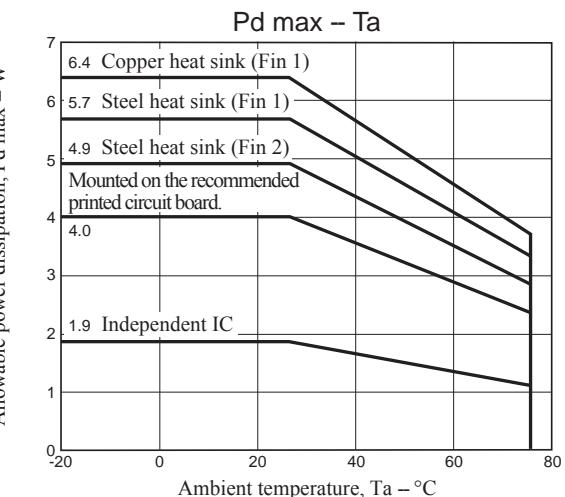
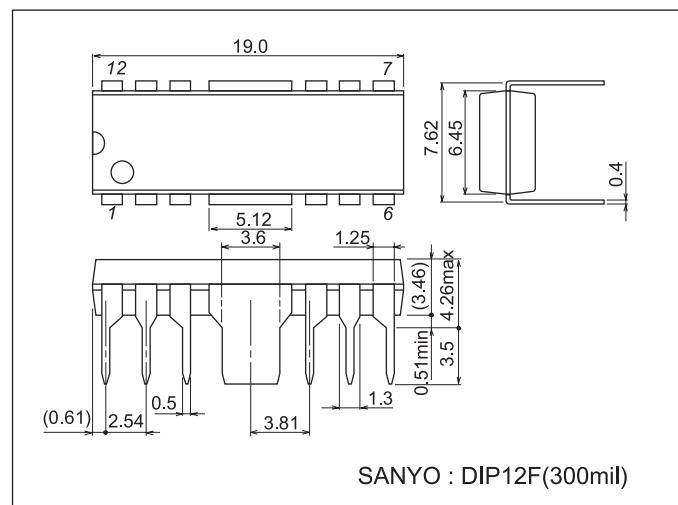
Electrical Characteristics at $T_a = 25^\circ\text{C}$, $V_{CC} = 9\text{V}$, $R_L = 3\Omega$, $f = 1\text{kHz}$, $R_g = 600\Omega$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Quiescent current	I _{CC0}	R _g = 0	17	30	70	mA
Voltage gain	V _G	V _O = 0dBm	43	45	47	dB
Total harmonic distortion	THD	P _O = 0.33W (V _O = 1.0V)		0.1	0.8	%
Output power	P _{O1}	THD = 10%	2.0	2.5		W
	P _{O2}	THD = 10%, R _L = 4Ω		2.0		W
Output noise voltage	V _{NO}	R _g = 0, DIN AUDIO		0.15	0.5	mVrms
Ripple rejection ratio	SVRR	R _g = 0, f _R = 100Hz, V _r = 0dBm, DIN AUDIO	45	52		dB
Channel separation	CHsep	R _g = 0, V _O = 0dBm, DIN AUDIO	50	60		dB
Standby current	I _{ST}	R _g = 0		1.0	10	µA
Input resistance	R _i		20	30	40	kΩ
Standby pin voltage	V _{ST}	The pin 1 voltage such that the amplifier is on	1.5	5.0		V

Package Dimensions

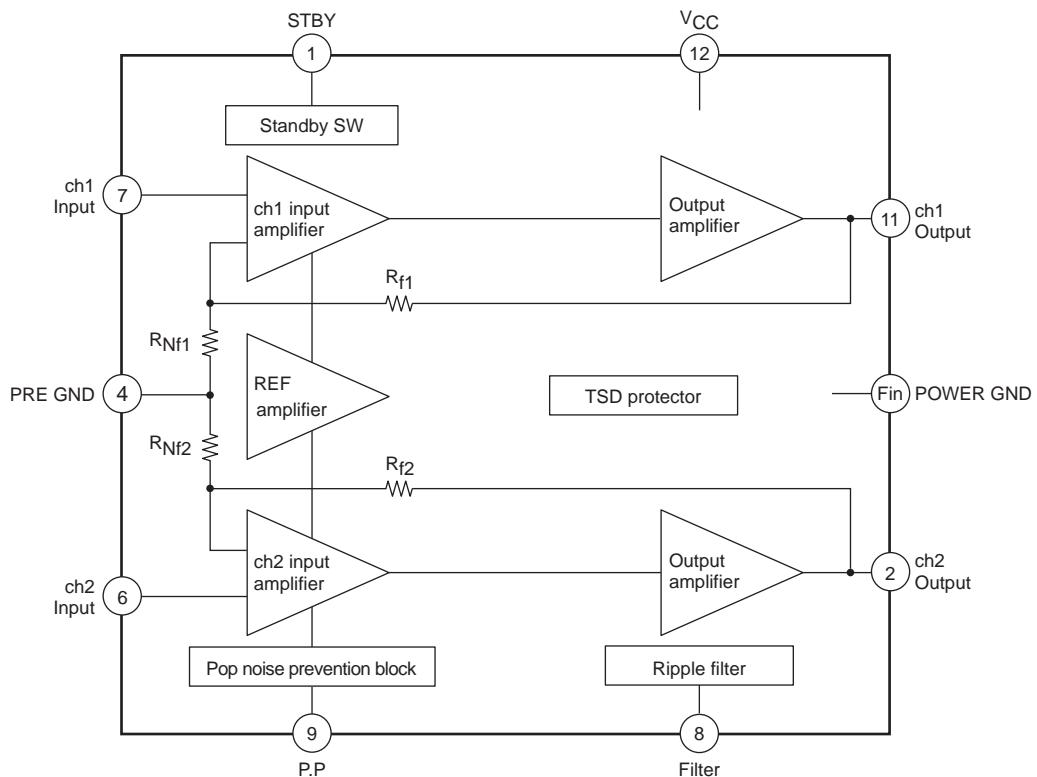
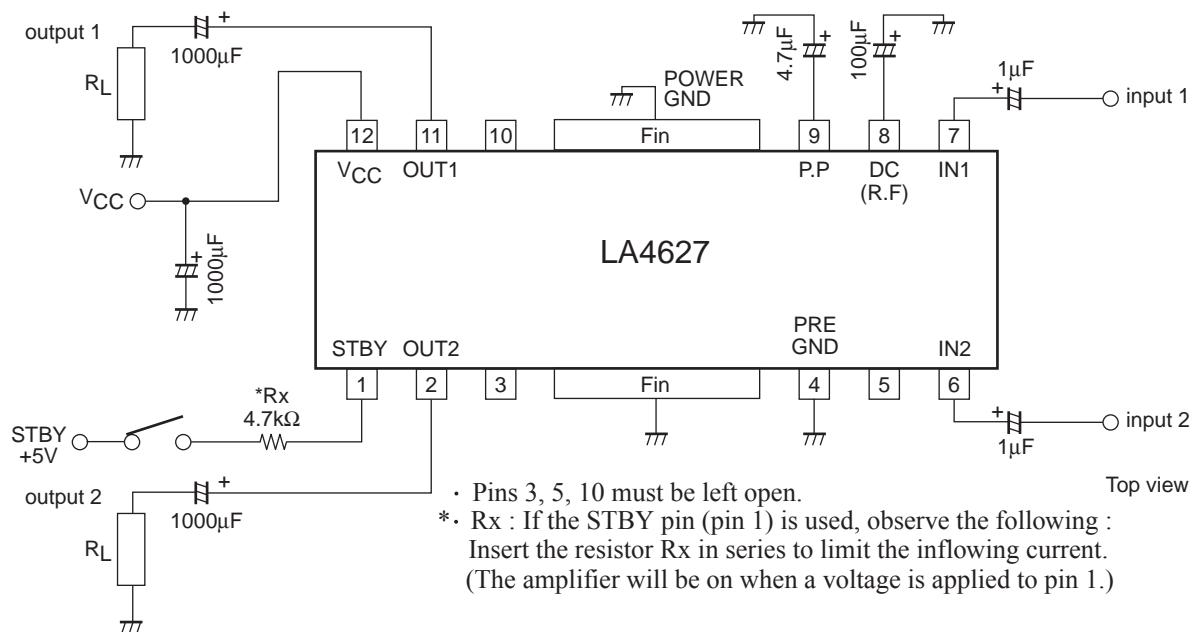
unit : mm (typ)

3022B



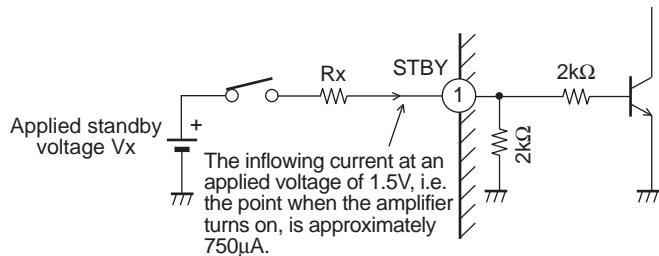
Fin 1

Fin 2

Block Diagram**Application Circuit Example**

Pin Functions

1. Standby switch function (pin 1)



STBY pin applied voltage : 5V

To hold the pin 1 inflow current to about 750µA insert a resistor (Rx) of 4.7kΩ

STBY pin applied voltage : 12V

To hold the pin 1 inflow current to about 750µA insert a resistor (Rx) of 14kΩ (12kΩ).

STBY pin applied voltage : Other value (Vx)

To hold the pin 1 inflow current to about 750µA insert a resistor (Rx) of $(Vx - 1.5V)/750\mu A$.

- If a microcontroller output signal is applied directly, insert a resistor in series and adjust the current to a level optimal for the drive capability of the microcontroller.

2. Input pins (pins 6 and 7)

The input pin voltage is about 2VBE (1.4V).

The input pin impedance is about 30kΩ.

- Although the recommended value for the input capacitor is 0.22µF, the starting time can be modified by changing the value of this capacitor. (The time from the point a voltage is applied to the standby pin to the point sound is emitted.)

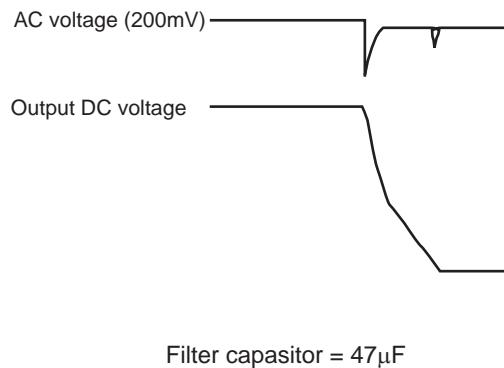
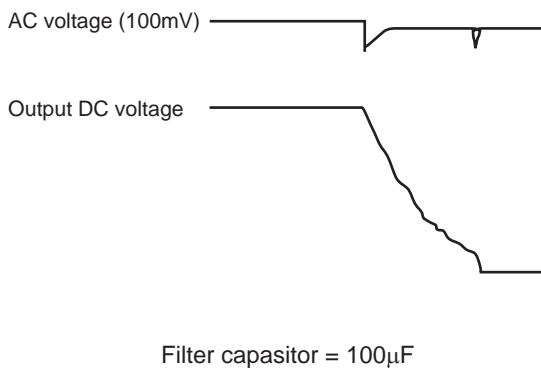
Input capacitor	1.0µF	2.2µF	3.3µF	4.7µF	10µF
Starting time (t _S)	0.2s	0.3s	0.5s	0.65s	1.5s

3. FILTER (decoupling) pin (pin 8)

The pin voltage is about $1/2V_{CC}$.

The recommended value for the filter capacitor is $100\mu F$.

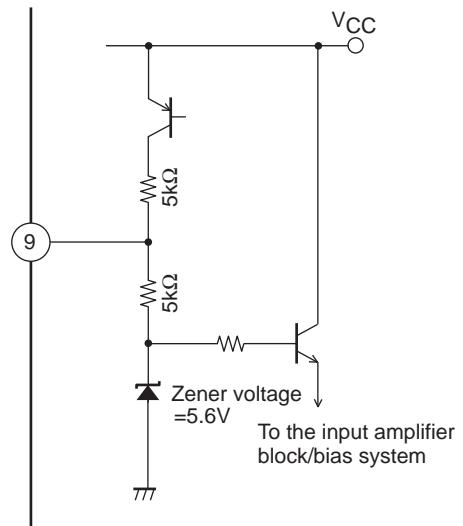
The pulse noise that occurs when the standby pin is set low (power off) will be degraded if a value under $100\mu F$ is used.



4. P.P (pulse noise) pin (pin 9)

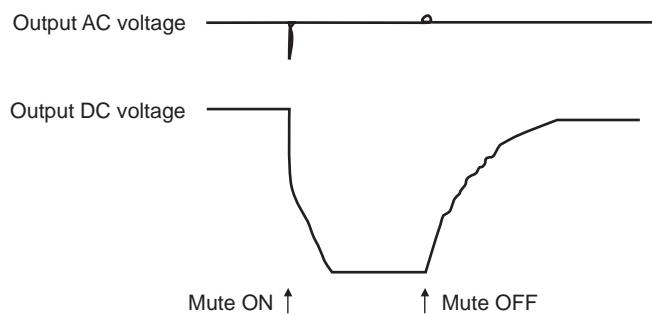
$$\text{Pin 9 pin voltage} \approx \frac{V_{CC} - V_{CE} (\text{about } 0.3V) - 5.6V}{2k\Omega} + 5.6V$$

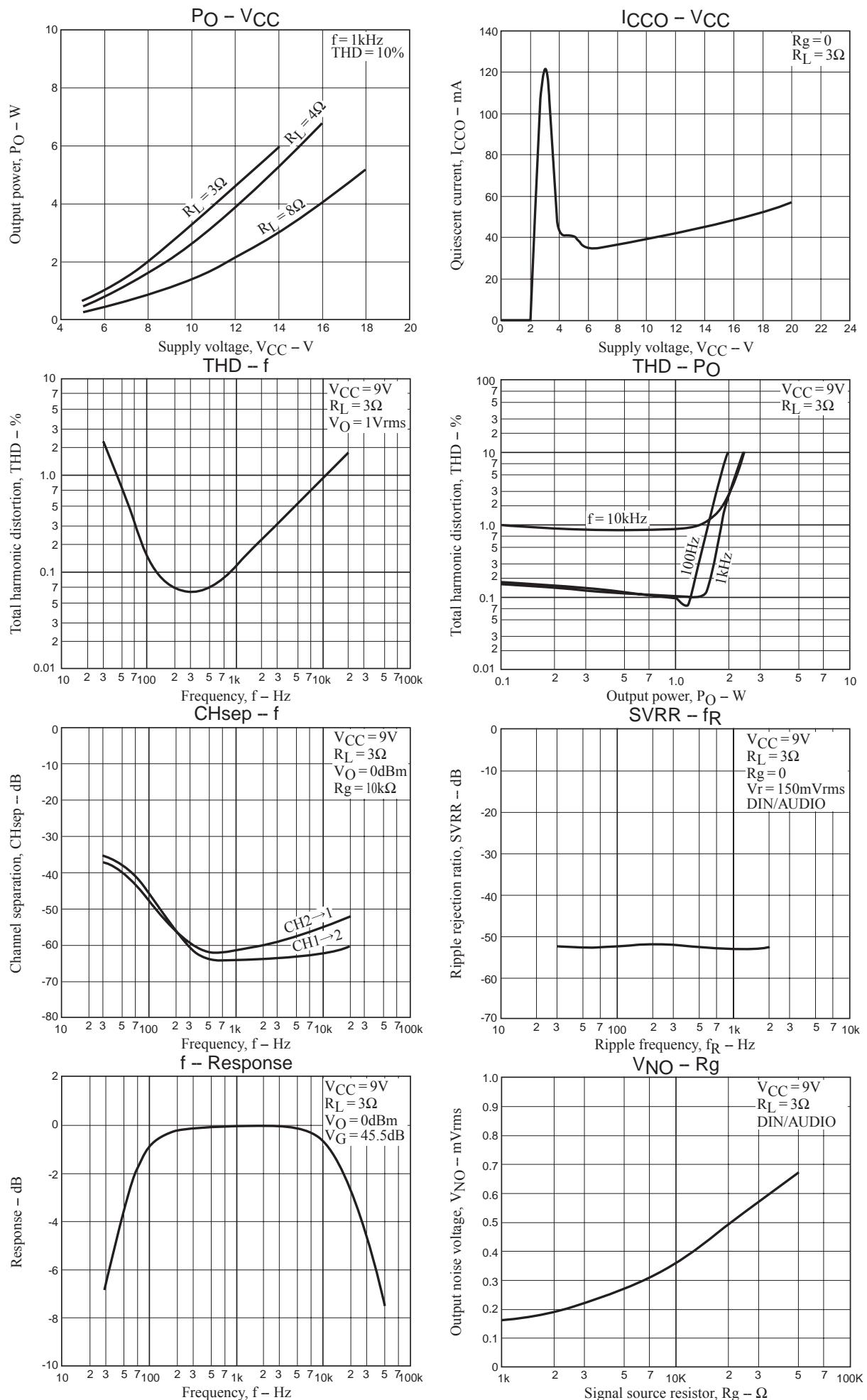
- The recommended value for the P.P capacitor is $4.7\mu F$.
The pulse noise that occurs when the standby pin is set low (power off) will be degraded if a value under $2.2\mu F$ is used.
Furthermore, if a value over $10\mu F$ is used, the signal may not be cut off and sound may remain audible when the standby pin is set low (power off).

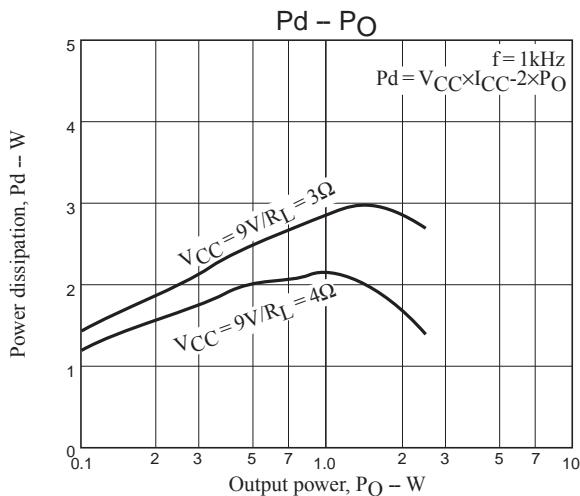


5. MUTE (Muting)

The output signal can be controlled by shifting the pin 8 (FILTER) level towards ground with a 300 to 500Ω resistor. However, note that the degree of suppression is reduced if a value of 750Ω or more is used.







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