

SANYO Semiconductors DATA SHEET

LA7151 LA7151M

Monolithic Linear IC Audio / Video Switch for VCR Video Camera Use

Overview

The LA7151 and LA7151M are high-performance, dual-channel audio / video switches designed for video camera applications.

The LA7151 and LA7151M have a wide bandwidth, low supply current and a large dynamic range, making them ideal for low-power or battery operated equipment.

The LA7151 and LA7151M operate from a 4.5 to 12.5V supply and are available in 12-pin SIPs and 10-pin MFPs, respectively.

Features

- Two, separately controllable switch circuits.
- $50k\Omega$ input impedance.
- Low supply current.
- · Large dynamic range.
- Wide bandwidth.
- 4.5 to 12.5V supply voltage.
- 12-pin SIP (LA7151) and 10-pin MFP (LA7151M).

Specifications

Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		15	V
Allowable power dissipation	Pd max	Ta≤80°C	150	mW
Operating temperature	Topr		-20 to +80	°C
Storage temperature	Tstg		-55 to +150	°C

Operating Conditions at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	Vcc		5, 9, 12	V
Supply voltage range	VCC op		4.5 to 12.5	V

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Operating Characteristics at Ta= 25° C, V_{CC}=5V

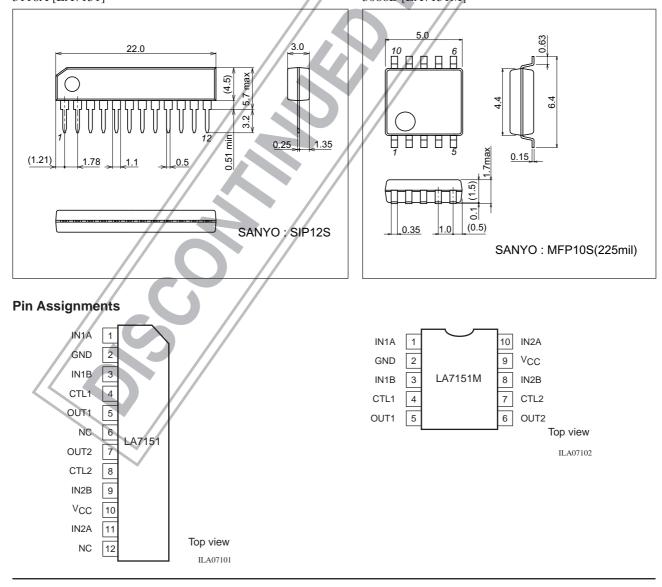
D	0.1.1	Symbol Conditions	Ratings			
Parameter	Symbol		min	typ	max	Unit
		No input, VCC=5V	5.5	7.0	8.5	mA
Current drain	ICC	No input, V _{CC} =9V	6.0	7.5	9.0	mA
		No input, V _{CC} =12V	6.5	8.0	9.5	mA
Total harmonic distortion	THD	VIN=1Vp-p,f=1kHz		0.006	0.1	%
Maximum output voltage	VOM	f=1kHz, THD=1%	2.2	2.5		Vp-p
Output noise voltage	VON	Rg=600Ω, DIN AUDIO filter		-110	-100	dB
Crosstalk between switches	CTa	Rg=50Ω, V _{IN} =2Vp-p, f=4.43MHz,		-60	-55	dB
	CTS	measured between switches A and B		-00	-00	
Crosstalk between channels	СТ _С	Rg=50Ω, VIN=2Vp-p, f=4.43MHz,		-65	-60	dB
Crossiaik between channels	CIC	measured between channels 1 and 2		-03	-00	uв
Second-harmonic distortion	H2	VIN=2Vp-p,f=4.43MHz		-50	-40	dB
Third-harmonic distortion	H3	VIN=2Vp-p,f=4.43MHz		-55	-45	dB
Frequency characteristic	Gf	VIN=2Vp-p,f=100kHz / 10MHz	-1	0	+1	dB
Voltage gain	VG	VIN=2Vp-p,f=4.43MHz	-0.3	0	+0.3	dB
Output DC offset	Vof	Output voltage difference when switching	-30	0	0 +30	mV
	VOI	between switches A and B		0		
Switch A input retention voltage	VCA	DC: CTL1, CTL2	3.5		5.0	V
Switch B input retention voltage	VCB	DC: CTL1, CTL2	0		1.5	V
Input impedance	Z _{IN}			50		kΩ
Output impedance	ZOUT			10		Ω

Package Dimensions

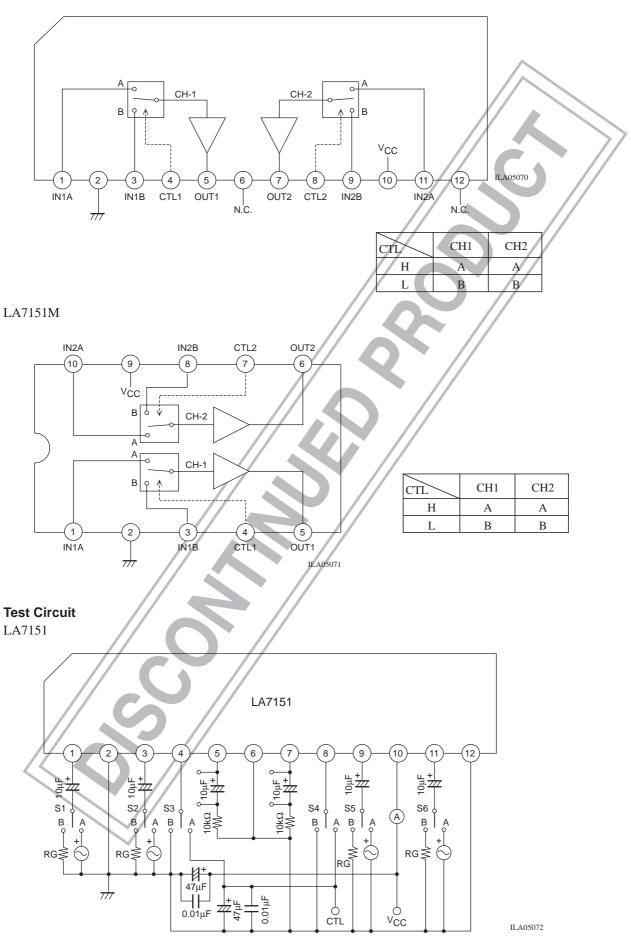
unit : mm 3116A [LA7151]



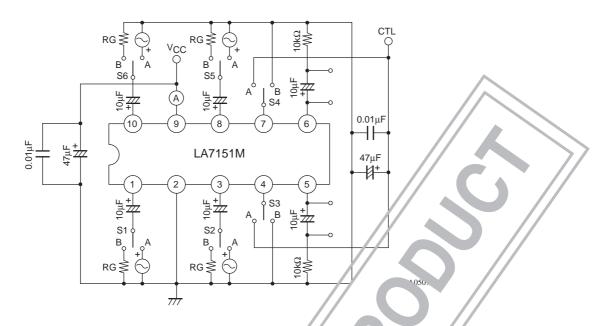
unit : mm 3086B [LA7151M]



Block Diagram LA7151



LA7151M



Pin Functions

Pin		Pin Name	Equivalent circuit	DC voltage	Description
SIP 1	MFP 1	IN 1A	Input rin V	(VCC 5V)	
3	3	IN 1B		3.10V	V _{CC} 9V: 5.78V
9	8	IN 2B			V _{CC} 12V: 7.79V
11	10	IN 2A	777 777 777 ILA05074		
2	2	GND		0V	
4	4	CTL 1	$\begin{array}{c} & 15k\Omega \\ & 15k\Omega \\ & 0 \\$		
8	7	C7L7	400 V 358 7777 7777 ILA05075		
5	5	OU'.		2.38V	V _{CC} 9V: 5.06V
7	6	Ou 2	Output pin	2.00 V	V _{CC} 12V: 7.07V
6 12		N.C.			OPEN or GND
10	9	VCC		5.0V	

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