Monolithic Linear IC

LA7845N

Vertical Deflection Output Circuit

[LA7845N]

180

Package Dimensions

unit : mm

3075-SIP7H

Overview

The LA7845N is a vertical deflection output IC for TVs and CRT displays with excellent image quality that use a BUS control system signal processing IC. This IC can drive the direct (even including a DC component) deflection voke with the sawtooth wave output from the BUS control system signal processing IC. When used in conjunction with Sanyo's LA7615 series of BUS control system signal processing ICs for TVs, this IC can process all color television signal system functions through the BUS system. Because the maximum deflection current is 2.2 Ap-p, the LA7845N is suited for large screen sets, being capable of driving screens ranging from 33 inches to 37 inches.

Features

- Low power dissipation due to built-in pump-up circuit
- Vertical output circuit
- Thermal protection circuit built in
- · Excellent crossover characteristics
- DC coupling possible

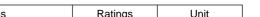
Specifications

Maximum Ratings at Ta = 25 °C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} 6 max		40	V
Output block supply voltage	V _{CC} 3 max		85	V
Deflection output current	l 2 max		-1.5 to +1.5	Ар-о
Thermal resistance	Өј-с		4.0	°C/W
Allowable power dissipation	Pd max	With arbitrarily large heat sink fins	11	W
Operating temperature	Topr		-20 to +85	°C
Storage temperature	Tstg		-40 to +150	°C

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SANYO : SIP7H

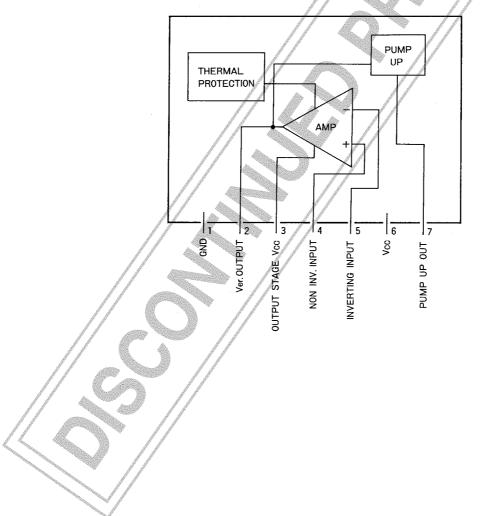
Operating Conditions at Ta = 25 $^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V _{CC} 6		24	V
Operating supply voltage range	V _{CC} 6 op		16 to 38	V
Recommended deflection output current	l 2 p-p		to 2.2	Ар-р

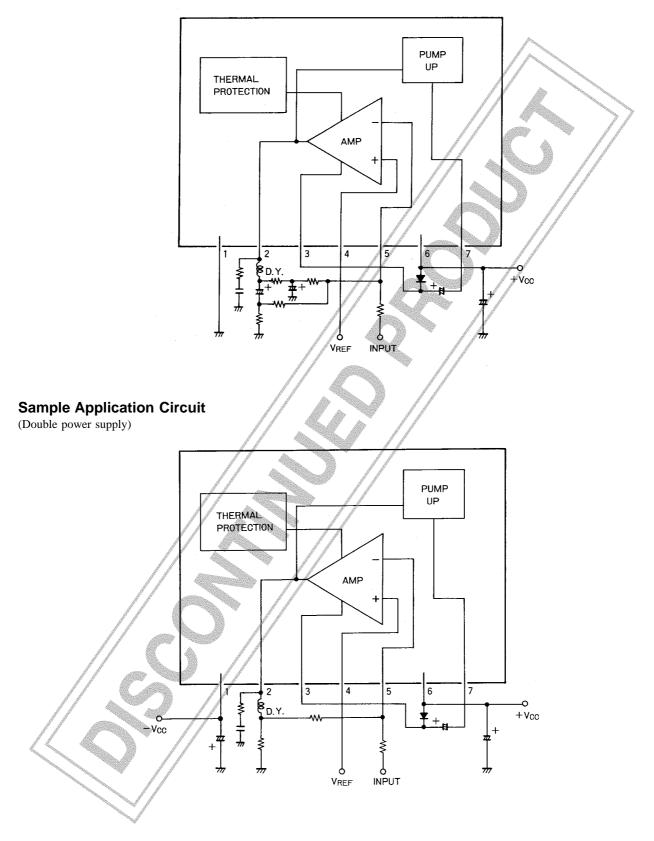
Operating Characteristics at Ta = 25 $\,^{\circ}C,\,V_{CC}6$ = 24 V

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Operating Characteristi	cs at Ta =	25 °C, V _{CC} 6	= 24 V		Å			>
Parameter	Symbol		Conditions		min	typ	max	Unit
Pump-up charge saturation voltage	V _S 7-1	l 7 = 20 mA		// (1.8	V
Pump-up discharge saturation voltage	V _S 6-7	I 7 = −1.1 A					3.2	V
Deflection output saturation voltage (lower)	V _S 2-1	I 2 = 1.1 A			Ð		1.5	V
Deflection output saturation voltage (upper)	V _S 3-2	I 2 = −1.1 A		$\overline{\mathbb{S}}$			3.5	V
Idling current	I _{DL}				35		70	mA
Midpoint voltage	V _{MID}			2	11.0	12.0	13.0	V

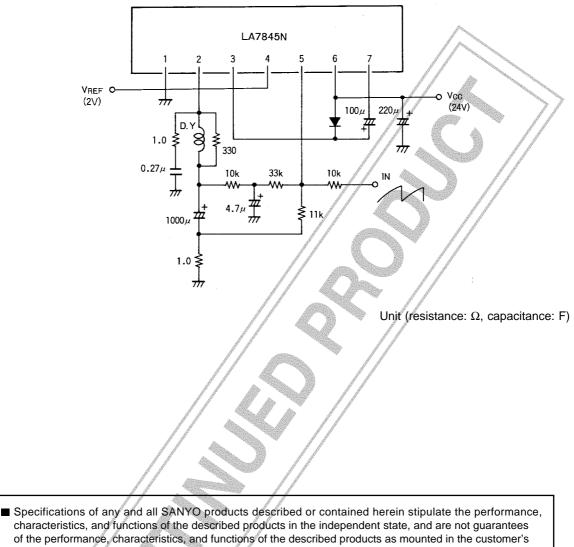
Block Diagram



Sample Application Circuit (Single power supply)



Sample Application Circuit



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