

□ MN152811

Type		MN152811	
ROM (×8-Bit)		8 K	
RAM (×4-Bit)		256	
Number of Instructions		115	
Minimum Instruction Execution Time		2.0 μs (at 4.5 V to 5.5 V, 6 MHz)	
Interrupts		• RESET • SIRQ • Remote Control Input • Timer • Serial (Only when choosing Mask Option)	
Timer Counter		Timer Counter : 8-Bit × 1 (Timer Output, Event Count) Clock Source 1/2, 1/8, 1/32, 1/128 of System Clock Interrupt Source Overflow of Timer Counter	
Serial Interface		Serial : 8-Bit × 1 Clock Source System Clock, $\overline{\text{SBT}}$ Pin Input	
I/O Pins	I/O	5	• Common use 2 • Specified pull-up Resistor available (Mask Option) 2 • Nch Open-Drain available (Output) 5
	Input	4	• Common use 1 • Specified pull-up Resistor available (Mask Option) 1 • Output selectable (Software Programmable) 3
	High Voltage Output	5	• Nch Open-Drain (Breakdown Voltage 12 V) 5 • Push-Pull Output selectable (Mask Option) 4
	Output	3	
A/D Inputs		5-Bit × 4ch (Conversion by Software)	
PWM		7-Bit × 6ch (Repetition Cycle 256 μs, at 6 MHz), 14-Bit × 1ch (Repetition Cycle 32.8 ms, at 6 MHz)	
Special Ports		Tri-State Output (PTO), Remote Control Reception	
CRTC		5 × 7 dots, 16 characters, 2 lines, 7 colors, 56 patterns, Rounding function, Framing function	
Notes		Remote Control Data Detection Circuit built-in, For Voltage Synthesizer	
Package		SDIP042-P-0600	

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Electrical Characteristics

Supply Current

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating Supply Current	IDD1	fosc = 6 MHz, VDD = 5 V Ta = 25 °C		5.0	10	mA
Supply Current at STOP	IDD3	fosc = 0 Hz, VDD = 5 V Ta = 25 °C			2.0	µA

(VDD = 5.0 V, VSS = 0 V)

A/D, D/A Converter Characteristics

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
A/D Conversion Absolute Error		VDD = 5 V, VSS = 0 V			±1	LSB
Analog Input Voltage			VSS		VDD	

(VDD = 5.0 V, VSS = 0 V, Ta = 25 °C, 80 °C)

Support Tool

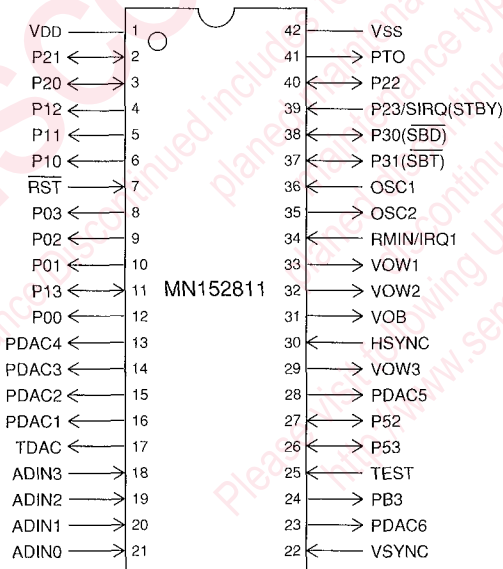
In-Circuit Emulator

PX-ICE1500 + PX-PRB152811

Piggyback

Use **EP152811**

Pin Assignment



SDIP042-P-0600

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