TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8464K

DUAL POWER OPERATIONAL AMPLIFIER

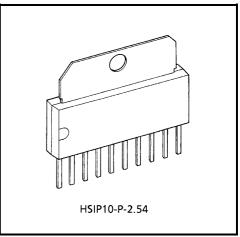
The TA8464K is a dual power operational amplifier with the output current 1.2 A (PEAK).

This amplifier is usable for CD player arm driver, brushed motor forward / reverse rotation control driver, and FDD / HDD voice coil motor.

Furthermore, this amplifier is best suited for LDP focus tracking actuator driver because of its high through rate.

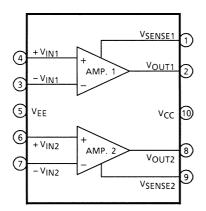
FEATURES

- Provided with a Current Limiter.
- High Output Current : IO (PEAK) = 1.2 A
- Internal Phase Compensation Type.
- Less Crosstalk : $C_T = 55 \text{ dB}$ (Typ.)
- High Slew Rate $: SR = 1.0 V / \mu s$ (Typ.)



Weight: 2.47 g (Typ.)

BLOCK DIAGRAM



PIN FUNCTION

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION			
1	V _{SENSE1}	ENSE1 AMP. 1 output current detective terminal			
2	V _{OUT1}	AMP. 1 output terminal			
3	-V _{IN1}	AMP. 1 input terminal (-)			
4	+V _{IN1}	AMP. 1 input terminal (+)			
5	VEE	Negative-side voltage supply terminal			
6	+V _{IN2}	AMP. 2 input terminal (+)			
7	-V _{IN2}	AMP. 2 input terminal (-)			
8	V _{OUT2}	AMP. 2 output terminal			
9	V _{SENSE2}	AMP. 2 output current detective terminal			
10	V _{CC}	Positive-side voltage supply terminal			

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	V_{CC}, V_{EE}	±18	V	
Output Current	I _{O (PEAK)}	1.2	А	
Power Dissipation	PD	12.5 (Note)	W	
Operating Temperature	T _{opr}	-30~75	°C	
Storage Temperature	T _{stg}	-55~150	°C	

Note: Tc = 25°C

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, V_{CC} = 15 V, V_{EE} = -15 V, Ta = 25°C)

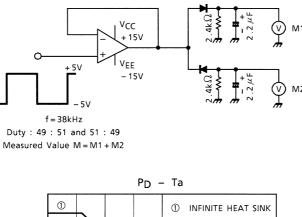
CHARACTERISTIC		SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Supply Current		ICC	_	—	_	17	25	mA
Input Offset Current		l _{IO}	_	—	_	3	100	nA
Input Bias Current		lı	_	—	_	98	300	nA
Input Offset Voltage		V _{IO}	_	—	_	0	7	mV
Maximum Output Voltage	Upper	V _{OH}	_	V _{CC} = ±15 V, I _O = 300 mA	12.2	13.3	_	v
	Lower	V _{OL}			-12.2	-13.3		
	Upper	V _{OH}		V _{CC} = ±6 V, I _O = 1 A	2.0	3.9		v
	Lower	V _{OL}			-2.0	-4.0		
Open Loop Gain		G _{VO}	—	_	_	80	_	dB
Input Common Mode Voltage Range		CMR	_	—	±13	±14		V
Common Mode Rejection Ratio		CMRR	_	V _{IN} = -10~10 V	90	113		dB
Supply Voltage Rejection Ratio		SVRR	_	V _{CC} = -V _{EE} = 6~15 V±1 V	_	65	100	μV / V
Slew Rate		SR	_	—	_	1.0		V / µs
Output Limiting Current		I _{SC}	_	R _{SC} = 0.68 Ω	0.8	1.0		А
Crosstalk		CT	_	V _{IN} = -14~14 V	_	55		dB
Slew Rate Symmetry		SR'	1	INPUT:Duty (49:51 / 51:49) Square wave	_	0.02	1.0	V

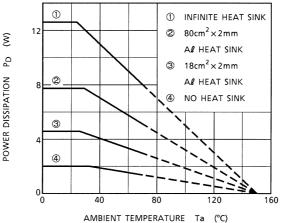


TEST CIRCUIT 1

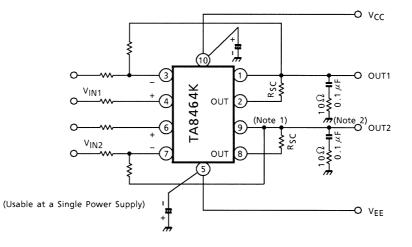
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Slew rate, symmetry SR'





APPLICATION CIRCUIT 1

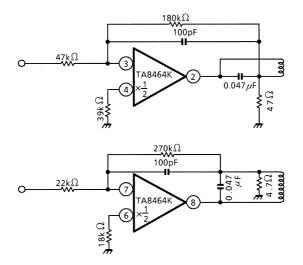


Note 1: $I_{SC} \approx \frac{0.7(V)}{R_{SC}(\Omega)}(A)$

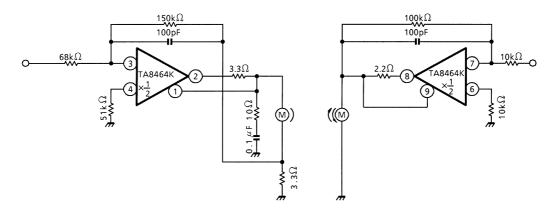
- Note 2: If crosstalk is recognizable remarkably in applications above 80 kHz, change a capacitor to one having a value of about 0.33 μF as a compensating circuit. Further, no resistor is needed in this case.
- Note 3: Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

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APPLICATION CIRCUIT 2



APPLICATION CIRCUIT 3

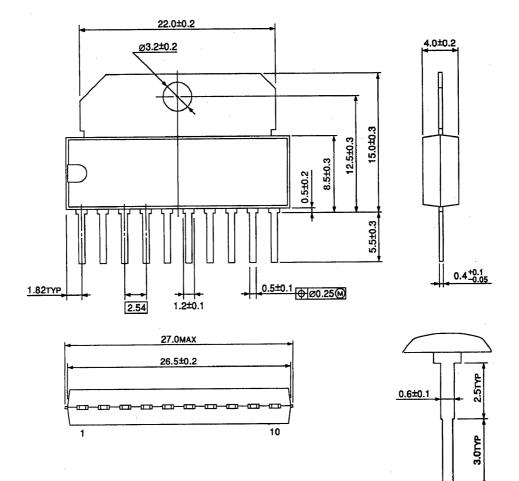


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PACKAGE DIMENSIONS

HSIP10-P-2.54

Unit : mm



Weight : 2.47 g (Typ.)

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