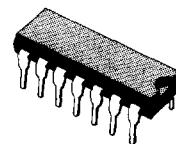




VIDEO AND AUDIO SIGNALS SWITCHING FOR THE PERI-TELEVISION PLUG

- VIDEO CROSSTALK : 60 dB TYPICAL
- LOW IMPEDANCE VIDEO OUTPUT 75 Ω
- SHORT-CIRCUIT PROTECTION OF INPUTS AND OUTPUTS
- INTERNAL HORIZONTAL PLL TIME CONSTANT SWITCHING IN CASE OF VIDEO RECORDER RECEPTION



DIP14
(Plastic Package)

ORDER CODE : TEA1014

DESCRIPTION

This integrated circuit provides both video and sound switching allowing connections between the peri-TV plug and video, sound sections in the TV set.

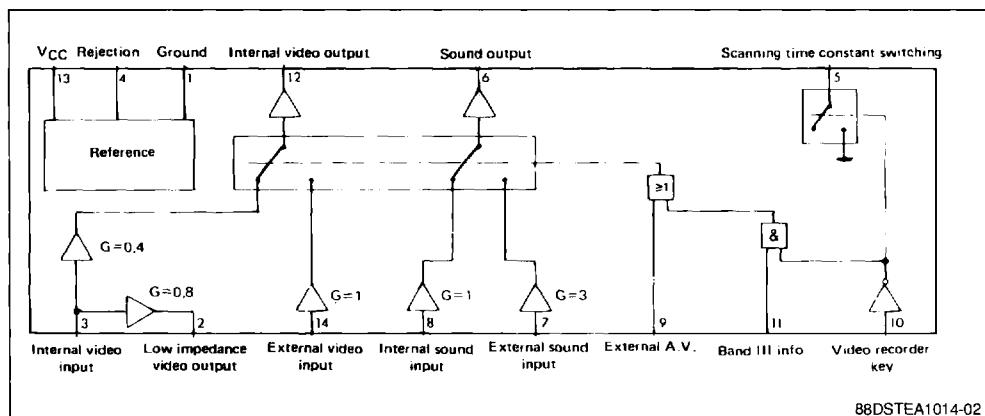
Input and output signal characteristics follow the NFC 92250/EN 50049 norms.

PIN CONNECTIONS

GROUND	<input type="checkbox"/> 1	14 <input type="checkbox"/> EXTERNAL VIDEO INPUT
EXTERNAL VIDEO OUTPUT	<input type="checkbox"/> 2	13 <input type="checkbox"/> SUPPLY
INTERNAL VIDEO INPUT	<input type="checkbox"/> 3	12 <input type="checkbox"/> INTERNAL VIDEO OUTPUT
REFERENCE	<input type="checkbox"/> 4	11 <input type="checkbox"/> BAND III INFORMATION
TIME CONSTANT SWITCHING	<input type="checkbox"/> 5	10 <input type="checkbox"/> VIDEO RECORDER KEY
SOUND OUTPUT	<input type="checkbox"/> 6	9 <input type="checkbox"/> VIDEO AND SOUND SWITCHING
EXTERNAL SOUND INPUT	<input type="checkbox"/> 7	8 <input type="checkbox"/> INTERNAL SOUND INPUT

91DSTEA1014-01

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC}	Supply Voltage	18	V
T _{stg}	Storage Temperature Range	- 40, + 150	°C
T _j	Junction Temperature	+ 150	°C
T _{oper}	Operating Ambient Temperature Range	0 to 70	°C

THERMAL DATA

R _{th(j-a)}	Junction Ambient Thermal Resistance	90	°C/W
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ELECTRICAL OPERATING CHARACTERISTICS

V_{CC} = 12 V ; T_{amb} = + 25 °C (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit
	Supply Current, with no Load		37	50	mA
	Internal Video Input (coming from picture IF) (pin 3) Video Signal Amplitude (positive video) Input Voltage Range (referred to D. C. input Voltage) Input Impedance Input Capacitance	- 2.9 5	2.5	6.8 + 3.9 5	Vpp V kΩ pF
	External Video Input (coming from peri-TV plug) (pin 14) Video Signal Amplitude (positive video) Input Voltage Range (referred to D. C. input Voltage) Input Impedance Input Capacitance	- 1.2 5	1	2.8 + 1.6 5	Vpp V kΩ pF
	TV Video Output (pin 12) Signal Amplitude Output Voltage Swing (referred to D. C. output Voltage) Output Dynamic Impedance D.C. Output Voltage (without input signal) Loading Resistance Video Bandwidth (- 1 dB)	- 1.2	1	2.8 + 1.6 10 3.5	Vpp V Ω V Ω MHz

ELECTRICAL OPERATING CHARACTERISTICS (continued)V_{CC} = 12 V ; T_{amb} = + 25 °C (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit
	Gain/internal Video Gain/external Video	- 9.5 - 1.5	- 8 0	- 6.5 + 1.5	dB dB
	External Video Output (low impedance) (pin 2) Signal Amplitude (on 150 Ω grounded) Output Voltage Swing Dynamic Output Impedance D.C. Output Voltage (without input signal) Minimum Loading Resistance (electrical performance non specified) Gain/internal Video	- 2.4	2 10 3.5	5.5 + 3.1	V _{pp} V Ω V Ω
	Output Video Signals Characteristics Video Rejection between two Inputs (1 MHz) Differential Group Delay Linearity Distortion Luma (test line 17) Chroma (test line 331) Intermodulation Luma-chroma (test line 331) Supply Voltage Rejection	- 55	2 2 5	20	dB ns %
	Internal Sound Input (pin 8) Input Signal Input Impedance		0.3 20	2	V _{RMS} kΩ
	External Sound Input (pin 7) Input Signal Input Impedance		0.1 20	0.7	V _{RMS} kΩ
	Sound Output (pin 6) Output Signal Amplitude Output Voltage Swing Distortion (V _O = 0.6 V _{eff}) Bandwidth Output Impedance Load Impedance Gain/internal Input Gain/external Input Supply Voltage Rejection Crosstalk Video/sound Crosstalk	16	0.3 2 40	0.5	V _{RMS} V _{RMS} % kHz Ω kΩ dB dB dB dB dB
	LOGIC External A. V. Input (peri-TV plug) (pin 9) Unactive Low Level or Unconnected Pin (logic state 0) – (TV receiving) Active High Level (logic state 1) (ext. receiving) Input Impedance	0 9		3 V _{CC} 10	V V kΩ
	"Band III" Input (pin 11) Unactive Low Level or Unconnected Pin (logic state 0) Active High Level (logic state 1) Input Impedance High Level Input Current Low Level	0 9		+ 3 V _{CC} 10	V V kΩ μA
	Video-recorder Key Input (pin 10) Unactive High Level or Unconnected Pin (logic state 1) Active Low Level (logic state 0) Input Impedance	9 0		V _{CC} 3 10	V V kΩ
	Open Collector Output (time-constant switching) (pin 5) Leakage Current (open collector) Maximum Low Level Voltage (I(5) = 4 mA)			1 1.5	μA V

TAB-04

CIRCUIT DESCRIPTION

The main functions of the I.C. are following :

VIDEO SWITCHING

2 electronically switched inputs :

- one 2.5 Vpp input for internal video.
- one 1 Vpp input for signal coming from the peri-TV plug.

2 outputs :

- 1 Vpp output (low impedance $75\ \Omega$) for peri-TV plug.
- 1 Vpp output low impedance for video section of the TV set.

Each input and output is protected from ground short-circuit. The $75\ \Omega$ output is protected through a $75\ \Omega$ resistor.

AUDIO SWITCHING

Two electronically switched inputs :

- 300 mV rms input coming from internal audio.
- 100 mV rms input coming from the peri-TV plug one low impedance output 300 mV rms.

Inputs and outputs are also protected against ground short-circuit.

SWITCHING LOGIC

The logic takes into account the information on pins.

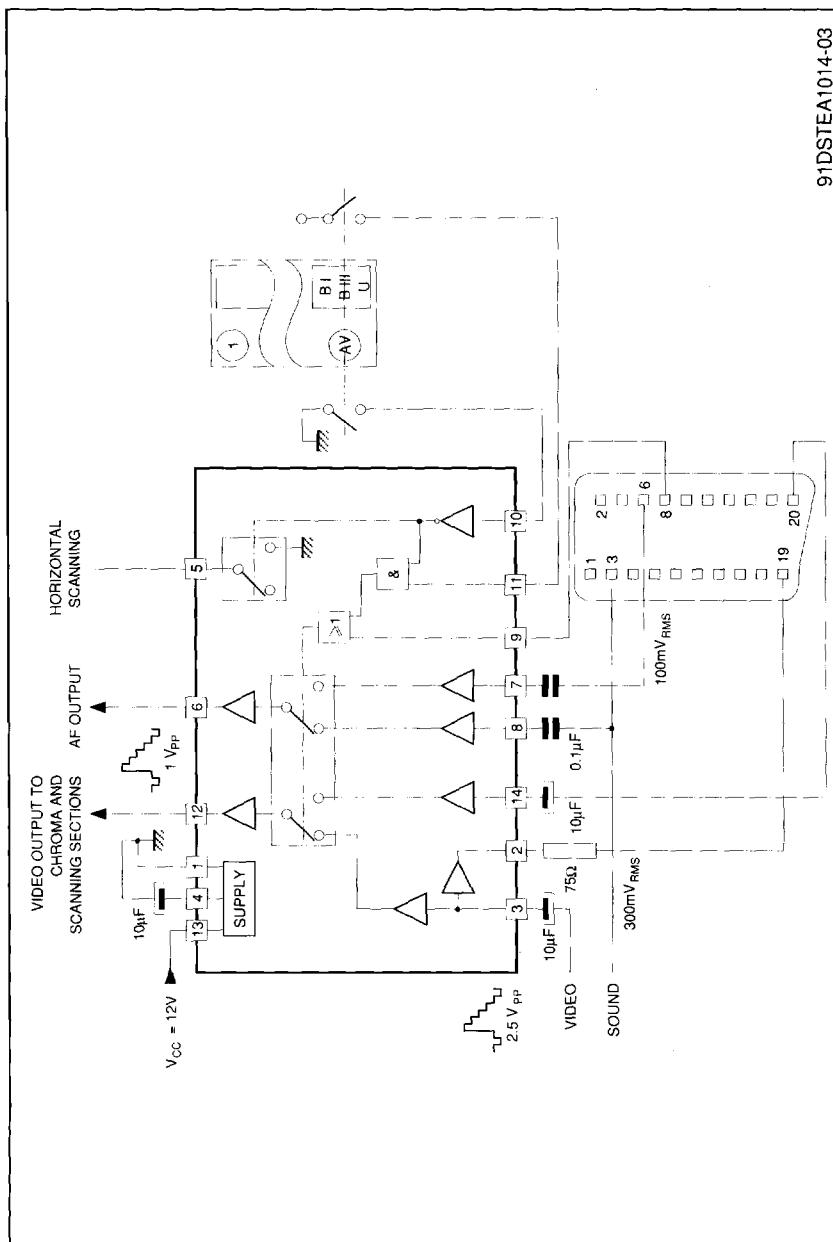
- Internal or external video and sound (pin peri TV plug)
- Band III information
- Video recorder key.

External Video and Audio signals are selected in two cases.

- When there is a voltage information coming from peri-TV plug.
- When the video recorder key is selected (c TV front panel) and programmed on band I

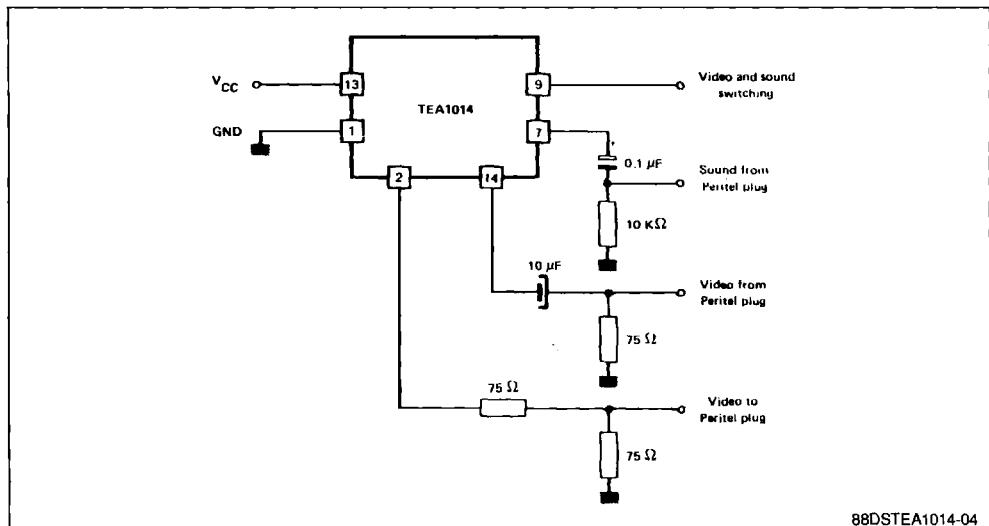
This I.C. includes an internal switch (open collector transistor) which commutes the time constant of the horizontal PLL circuit in case of video recorder reception.

APPLICATION CIRCUIT



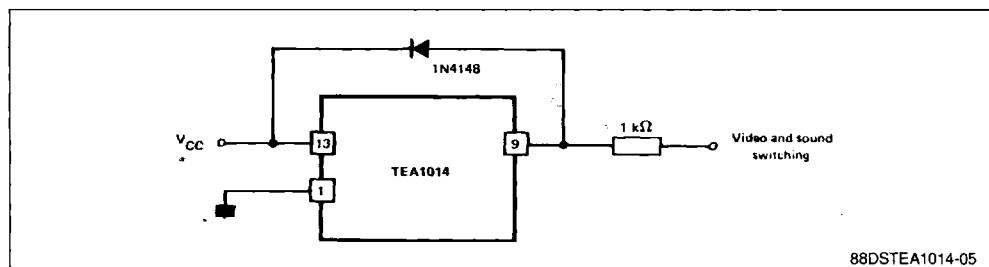
SAFETY INFORMATION FOR CRITICAL APPLICATIONS

Typical Connection Between Peritel Plug and TEA1014.



88DSTEA1014-04

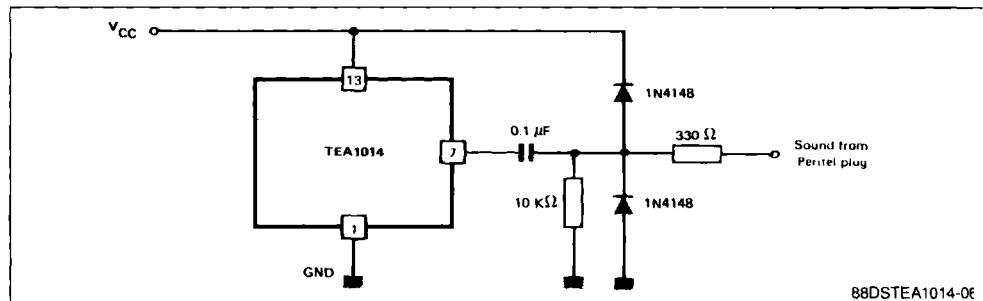
Voltage on pin 9 must not exceed the V_{CC} voltage on pin 13. In case of risk of over voltage, use the protection as described as below :



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All connections to Peritel plug are terminated by low impedance loads (75 Ω), except the external sound input.

In case of risk of electrostatic discharge, use the protection as described as below.



88DSTEA1014-06

PACKAGE MECHANICAL DATA

14 PINS – PLASTIC DIP

