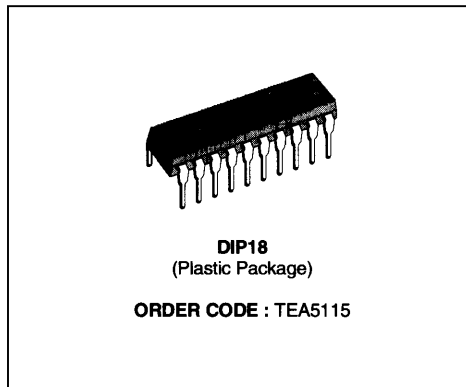
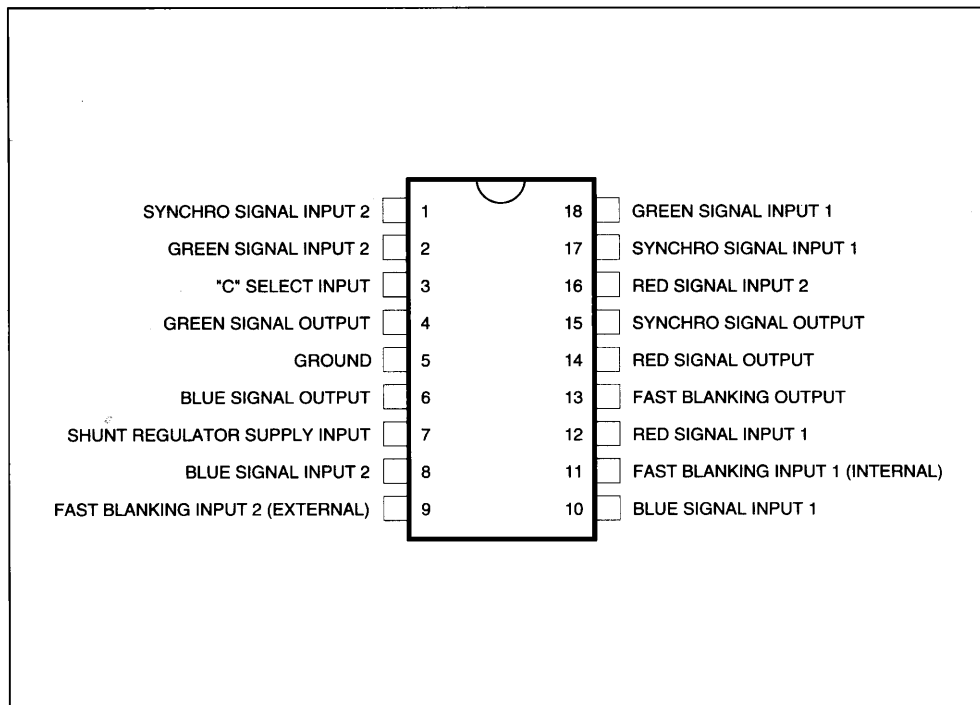


5 CHANNELS VIDEO SWITCH

- EACH CHANNEL EXCEPT FAST BLANKING HAS 6dB GAIN
- R, G, B AND VIDEO SIGNALS ARE CLAMPED TO THE SAME REFERENCE VOLTAGE IN ORDER TO HAVE NO OUTPUT DIFFERENTIAL VOLTAGE WHEN SWITCHING
- ALL INPUT LEVELS COMPATIBLE WITH NFC 92250 AND EN 50049 NORMS
- 30MHz BAND WIDTH FOR R, G, B SIGNALS
- INTERNAL 6.7V SHUNT REGULATOR FOR :
 - LOW IMPEDANCE LOADS,
 - POWER DISSIPATION LIMITATION
- INDEPENDANT VIDEO OR SYNCHRONIZING SIGNAL SELECTION
- SIMULTANEOUS SWITCHING OF R, G, B AND FB SIGNALS BY FB1 INPUT (internal)

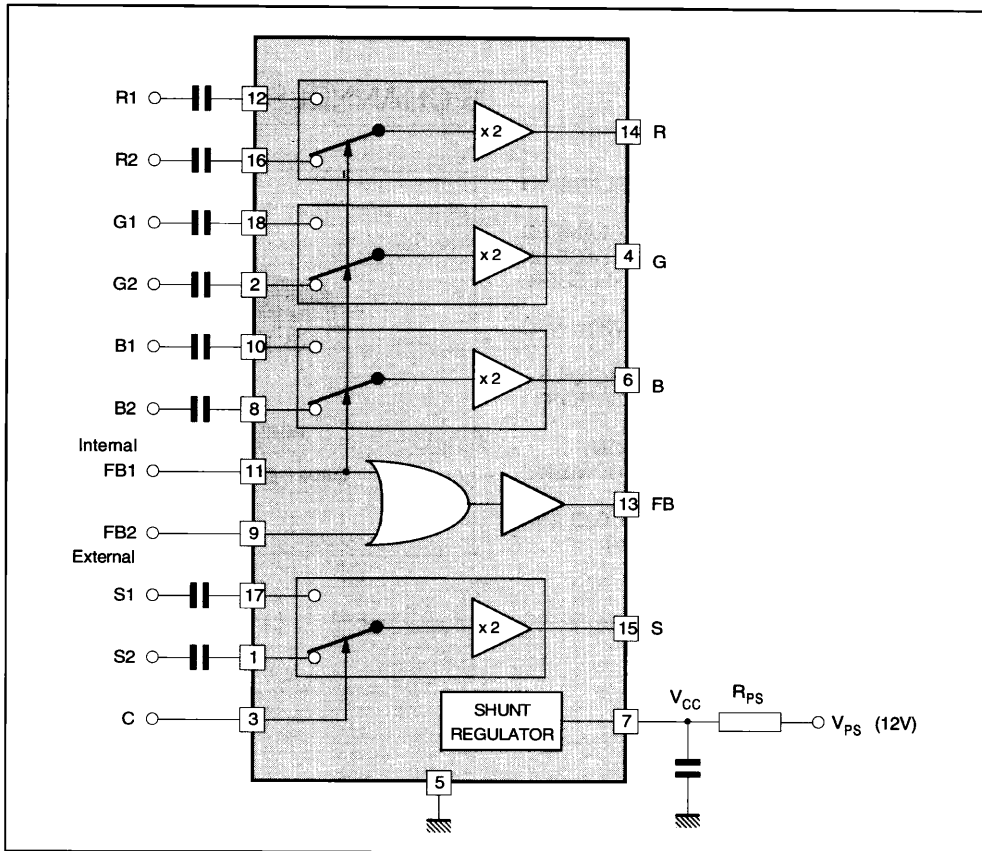


PIN CONNECTIONS



5115-01 EPS

BLOCK DIAGRAM



5115-02.EPS

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
I_{CC}	Supply Current (see note)	150	mA
V_{in}	Input Voltage (all inputs)	- 0.5 to $V_{CC} + 0.5$	V
T_{oper}	Operating Temperature Range	0 to 70	°C
T_j	Junction Temperature	- 40 to + 150	°C
T_{stg}	Storage Temperature	- 40 to + 150	°C

5115-01.TBL

Note : Minimum output load is 300 Ω in case of all outputs loaded.

THERMAL DATA

Symbol	Parameter	Value	Unit
$R_{th(j-a)}$	Junction-ambient Thermal Resistance	70	°C/W

5115-02.TBL

ELECTRICAL CHARACTERISTICST_{amb} = + 25 °C, I_{CC} = 120 mA ; Load value = 150 Ω

(sequentially switched) (unless otherwise specified, refer to test circuit page 7)

Symbol	Parameter	Min.	Typ.	Max.	Unit	
V _{CC}	Internal Shunt Regulator	I _{CC} = 120 mA	6.3	6.7	7.2	V
		I _{CC} = 90 mA	6.2		7.3	V
		I _{CC} = 150 mA	6.2		7.3	V

R, G, B Switches (pins 4, 6, 14) (Time Measurement Conditions : Δ inputs RGB = 0.7 V_{pp} ;
FB input pulse amplitude = 2 V)

V _C	DC Output Voltage (no input voltage)	T _{junction} = 25 °C T _{junction} stabilized		0.9 1.2	1.25	V
V _{AC}	Max Output Swing Voltage		2	4.0		V _{pp}
B	Bandwidth (– 3 dB) (input voltage 0.7 V _{pp})		20	30		MHz
A _v	Gain of Each Channel (input voltage 0.7 V _{pp} ; f = 1MHz)		5.5	6	6.5	dB
A _{dc}	Gain Difference Between any two R, G, B Channels (input voltage 0.7 V _{pp} ; f = 1 MHz)			0.1	0.5	dB
	Input Swing			0.7 V ± 3dB		
Z _{ic}	DC Input Impedance			10		kΩ
Z _{oc}	Dynamic Output Impedance (input voltage 0.7 V _{pp} ; f = 1MHz) with R _{load} = 300 Ω			10		Ω
	Crosstalk between any inputs (R1 and R2 or B1 and B2 or G1 and G2) (input voltage 0.7 V _{pp} ; f = 1 MHz).		45	55		dB
	Crosstalk between any outputs (input voltage 0.7 V _{pp} ; f = 1 MHz).		40	55		dB
t _{dc}	Delay time between R, G, B inputs and RGB outputs.			10		ns
t _{sr1}	Switching rise time between FB1 input signal and R, G, B output signal.			60	110	ns
t _{sf1}	Switching fall time between FB1 input signal and R, G, B output signal.			10	40	ns
t _{sr2}	Switching rise time between FB2 input signal and R, G, B output signal.			10		ns
t _{sf2}	Switching fall time between FB2 input signal and R, G, B output signal.			10		ns
t _{d11} t _{d12}	R1, G1, B1 Decay Time			30 60		ns ns
t _{d21} t _{d22}	R2, G2, B2 Decay Time			45 40		ns ns

Fast Blanking Switch (pin 13)

(time measurement conditions : FB input pulse amplitude = 2 V)

V _{IL}	Low Level Input Voltage FB1 and FB2	- 0.5			0.45	V
V _{IH}	High Level Input Voltage FB2 External	1			V _{CC} +0.5	V
V _{IH}	High Level Input Voltage FB1 Internal	1.2			V _{CC} +0.5	V
V _{OL}	Low Level Output Voltage				0.6	V
V _{OH}	High Level Output Voltage	T _{junction} = 25 °C T _{junction} stabilized	1.4 1.5	1.7 1.9	3.5	V V
	Input Current (without load)			1.5		μA
	Dynamic Output Impedance : with R _{load} = 300 Ω			10		Ω
t _{FB1r}	Switching rise time between FB1 input and FB output.			120	160	ns
t _{FB1f}	Switching fall time between FB1 input and FB output.			25	60	ns
t _{FB2r}	Switching rise time between FB2 input and FB output.			70		ns
t _{FB2f}	Switching fall time between FB2 input and FB output.			35		ns
cl _r	Delay Between RGB Output Signal and FB Output Signal (rise time)			50	100	ns
cl _f	Delay Between RGB Output Signal and FB Output Signal (fall time)			20	40	ns

5115CG.TBL

ELECTRICAL CHARACTERISTICS (continued)T_{amb} = + 25 °C, I_{CC} = 120 mA ; Load value = 150 Ω

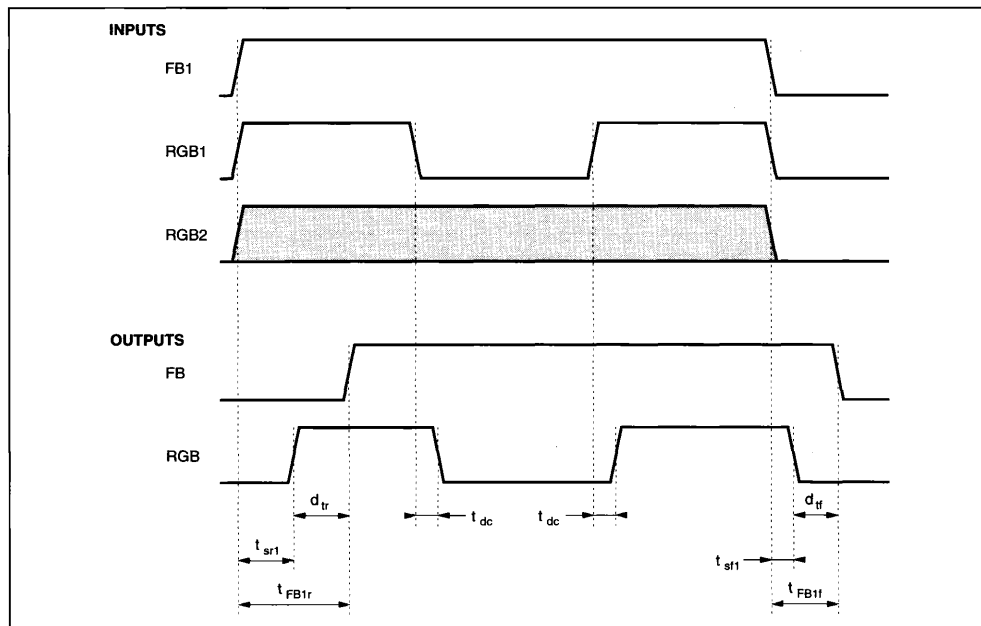
(sequentially switched) (unless otherwise specified, refer to test circuit page 7)

Symbol	Parameter	Min.	Typ.	Max.	Unit
Video (or synchro) Signal Switch (pin 15)					
V _S	DC Output Voltage (no input voltage)		0.9	1.25	V
	Max Output Swing Voltage		1.2		V
	DC Input Impedance	2.6	10		V _{pp} kΩ
	Dynamic Output Impedance (input voltage 1V _{pp} ; f = 1MHz) with R _{load} = 300 Ω		10		Ω
	Gain (input voltage 1 V _{pp} ; f = 1MHz) Bandwidth (- 3 dB) (input voltage 1 V _{pp})	5.5 15	6 20	6.5	dB MHz
	Input Swing		1V ± 3 dB		
t _{cr}	Switching rise time between C input signal and S output signal (C pulse amplitude 3 V).		30		ns
t _{cf}	Switching fall time between C input signal and S output signal (C pulse amplitude 3 V).		10		ns
t _{dc}	Delay Time Between S Input and S Output (Δ input 0.7 V _{pp})		10		ns

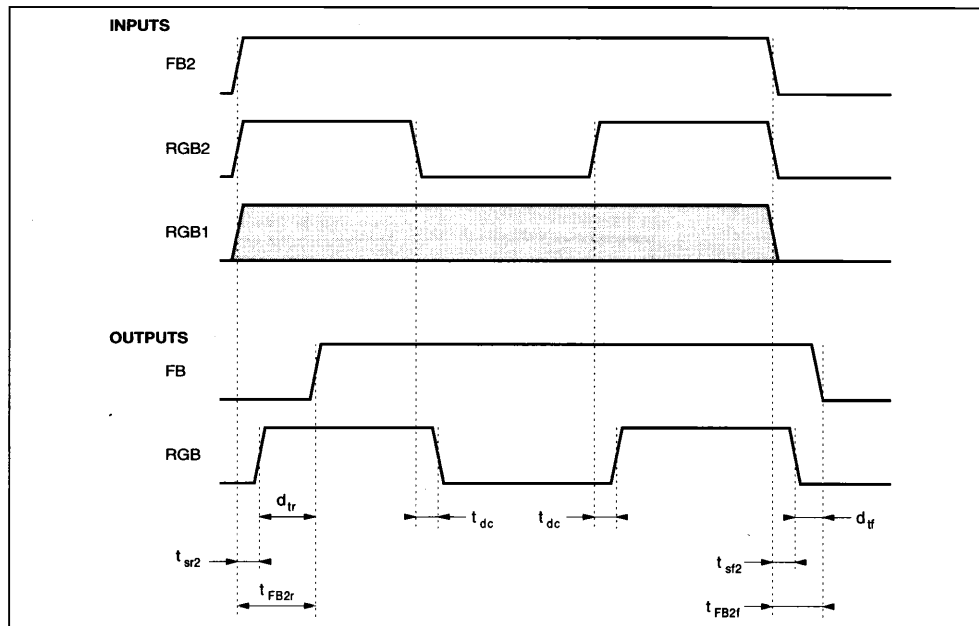
Select Input "C" (pin 3)

V _{IL}	Low Level Input Voltage	- 0.5		1	V
V _{IH}	High Level Input Voltage	2		V _{CC} +0.5	V
I _{IL}	Low Level Input Current (V _{IL} = 1 V)	- 0.6		- 0.1	mA
I _{IH}	High Level Input Current (V _{IH} = 3 V)			0.5	mA

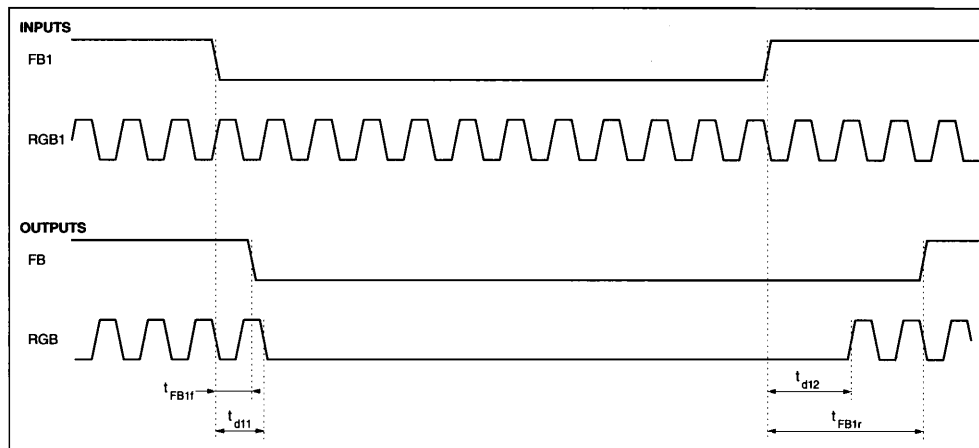
FB2 = 0



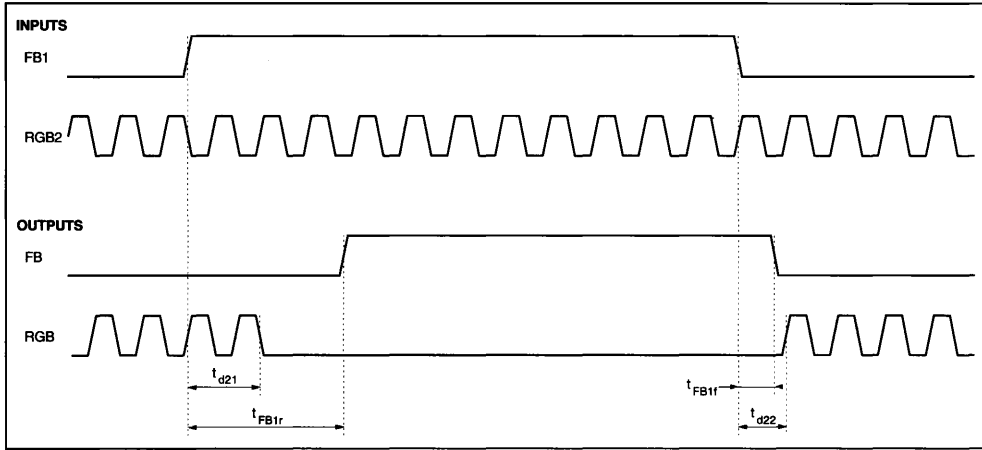
FB1 = 0



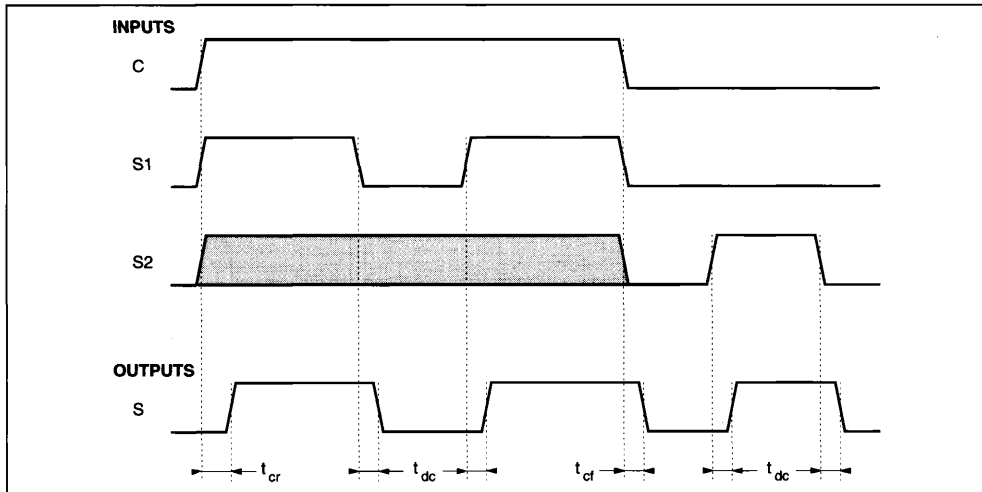
RGB2 = 0, FB2 = 0



RGB1 = 0, FB2 = 0

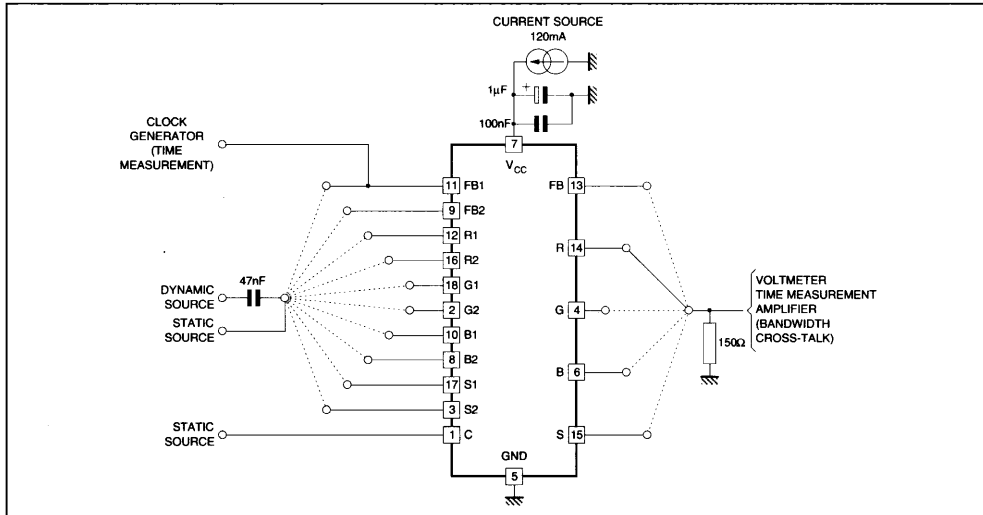


5115-06.EPS



5115-07.EPS

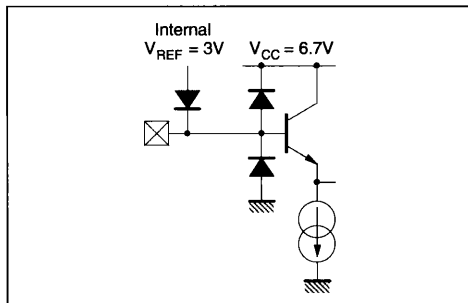
TEST CIRCUIT



5115-08.EPS

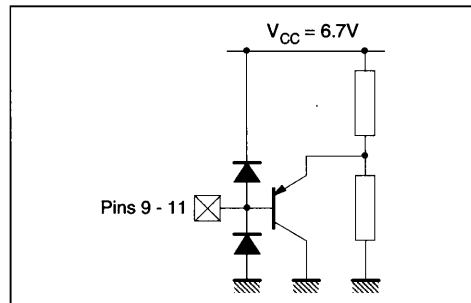
INPUTS/OUTPUTS EQUIVALENT INTERNAL DIAGRAMS

R, G, B, S inputs (pins 1, 2, 8, 10, 12, 16, 17, 18)



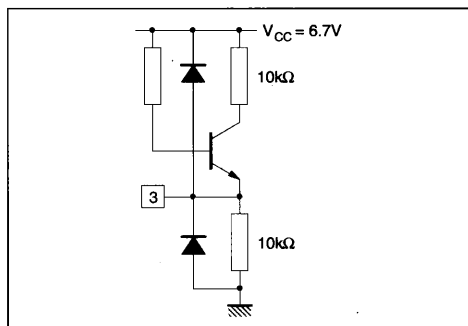
5115-09.EPS

FB inputs (pins 9, 11)



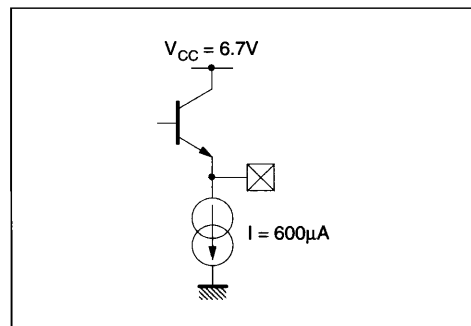
5115-10.EPS

C input (pin 3)



5115-11.EPS

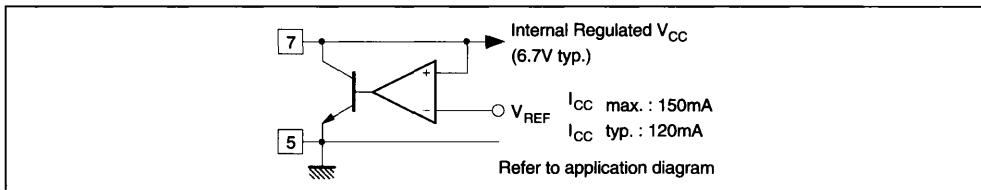
All Outputs (pins 4, 6, 13, 14, 15)



5115-12.EPS

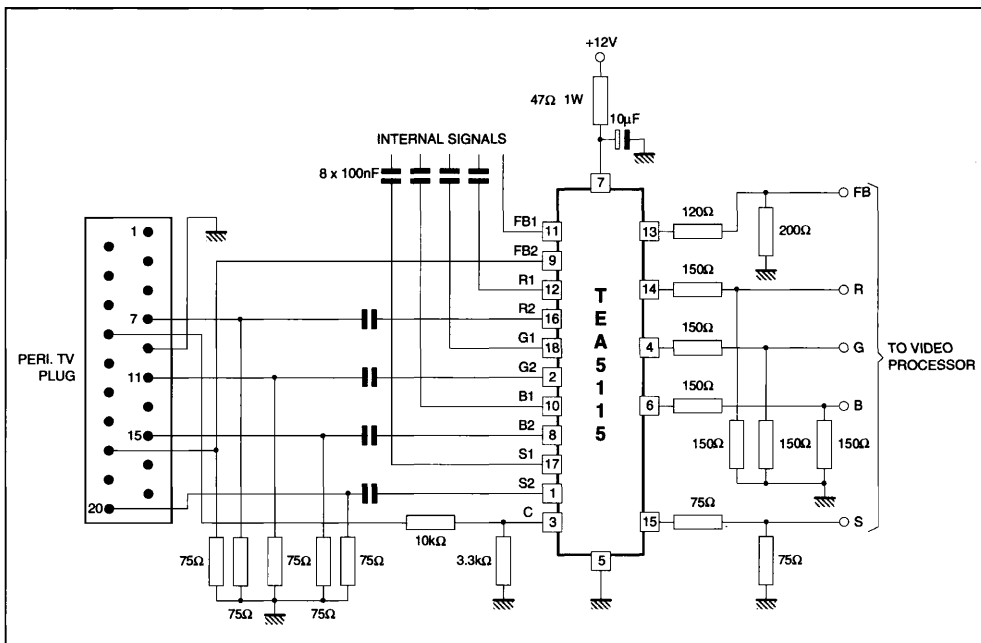
INPUTS/OUTPUTS EQUIVALENT INTERNAL DIAGRAMS (continued)

I_{CC} Supply (shunt transistor regulation system) (Pin 7)



5115-13.EPS

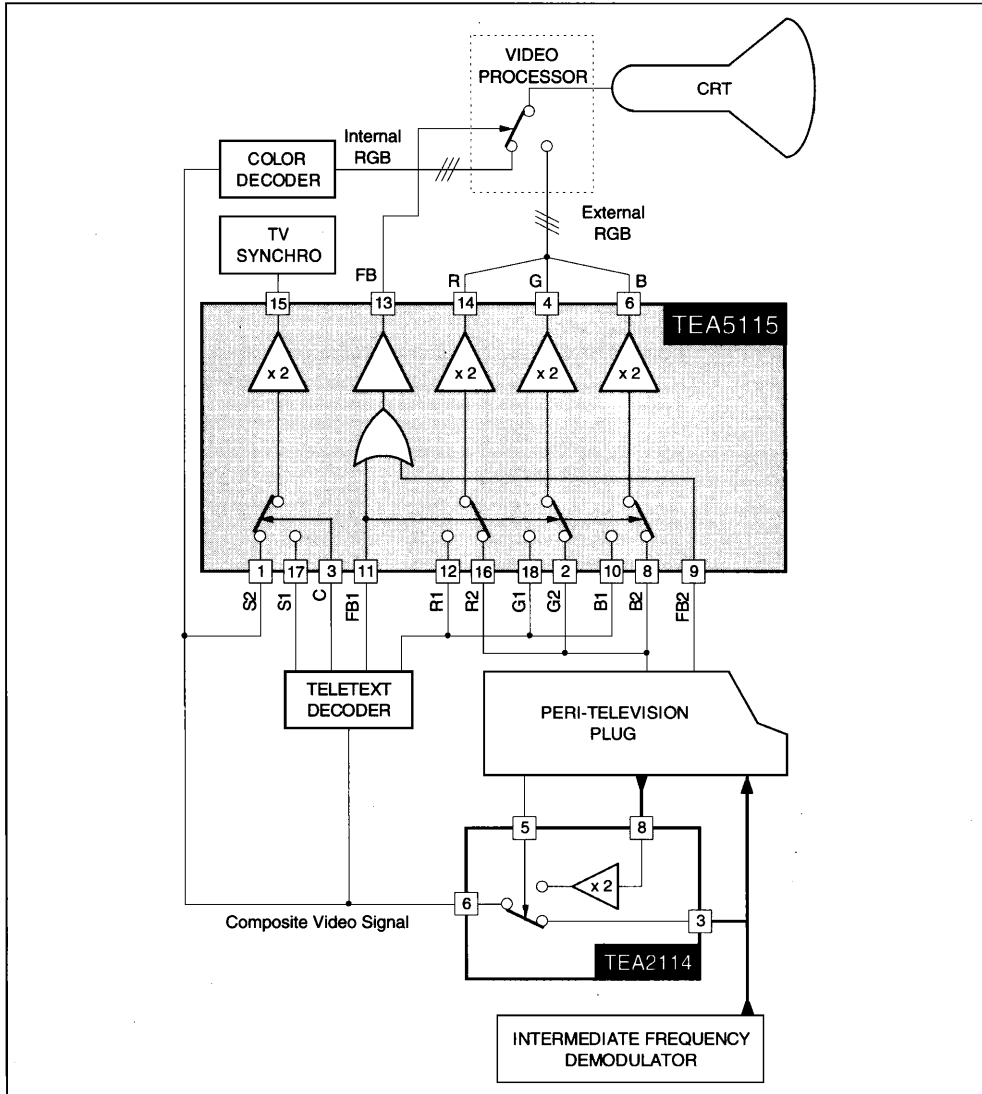
TYPICAL APPLICATION DIAGRAM



5115-14.EPS

- Above given output load values are minimum values, in case of all output loading.
- Minimum output load is 150 Ω individually, provided that total supply current is less than 150 mA.

TELETEXT SWITCHING APPLICATION WITH TEA5115 AND TEA2114



5115-15EPS