

# SANYO Semiconductors DATA SHEET

# LA7950 — TV Field Frequency Discriminator

#### Overview

The LA7950 is an IC designed to discriminate the field frequency based on the relation between TV vertical signal and horizontal signal. It is suited for use in automatically setting various types of video equipment, such as color TV, to PAL/NTSC mode.

#### **Features**

- Small-sized package (SIP10).
- Stable to external disturbance such as noise.
- Many output pins for mode select (4 outputs).
- Wide operating voltage range (6 to 13V).

#### **Functions**

- Sync separation.
- Field frequency counter.
- Vertical sync separation.
- Forced (manual) output select.

#### **Specifications**

**Maximum Ratings** at  $Ta = 25^{\circ}C$ 

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>2</sub> max		14	V
Load current	l <sub>7, 8, 9, 10</sub>		20	mA
Allowable power dissipation	Pd max	Ta ≤ 60°C	300	mW
Operating temperature	Topr	/ //	-20 to +85	°C
Storage temperature	Tstg		-40 to +125	°C

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#### **Operating Conditions** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V <sub>2</sub>		12	V
Operating voltage range	V <sub>2</sub> op		6 to 13.2	V

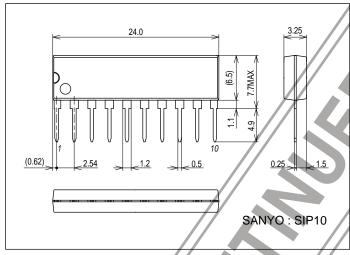
### **Electrical Characteristics** at Ta = 25°C, $V_2 = 12V$ , See Test Circuit

Darameter	Symbol	O and distance		Ratings		
Parameter		Conditions	min	typ	max	Unit
V <sub>CC</sub> 2 current drain	I <sub>CC</sub> 2	Pin 1 GND	8	12	16	mA
Sync separation output voltage	٧3	20kΩ across pin 4 and GND	10.5	11.0	11.5	V
Pin 1 DC voltage	V <sub>DC</sub> 1		4.8	5.3	5.8	V
Forced PAL operation start voltage	V <sub>POS</sub>				11.2	V
Forced PAL nonoperation max. votlage	V <sub>PNH</sub>		8.6			V
Forced NTSC operation start voltage	V <sub>NOS</sub>		0.4			V
Forced NTSC nonoperation min.	$V_{NNL}$				2	V
voltage						
Output saturation voltage	V <sub>7, 8, 9, 10</sub>	I <sub>7</sub> , 8, 9, 10 = 10mA			0.7	V

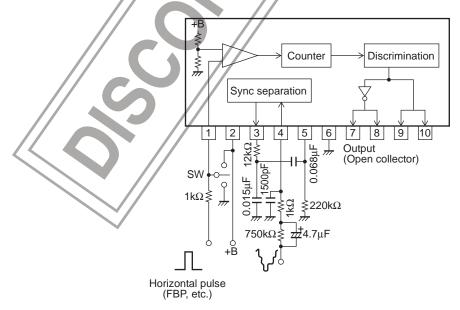
## **Package Dimensions**

unit: mm (typ)

3043C

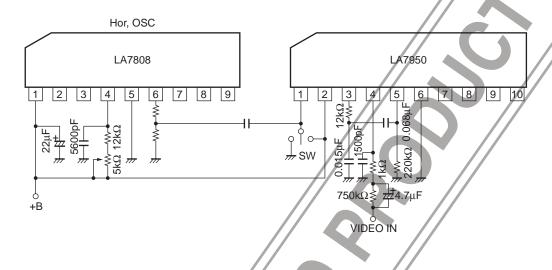


# **Block Diagram and Sample Application Circuit 1**



		Discrimination of field frequency	Output		
		Discrimination of field frequency	Pins 7, 8	Pins 9, 10	
Number of horizontal scanning lines per field	240 to 287	60Hz (NTSC)	off	on	
(Number of horizontal pulses)	288 to 340	50Hz (PAL)	on	off	
Forced select SW	GND	60Hz (NTSC)	off	on	
	+B	50Hz (PAL)	on	off	

### Sample Application Circuit 2: Using dedicated clock (LA7808)



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