

SANYO Semiconductors

DATA SHEET

LB3500 Monolithic Digital Integrated Circuit 1/8 Prescaler for PLL Electronic Tuning

The LB3500 is an ECL prescaler with 1/8 frequency division ratio developed for use in PLL electronic tuning FM radio application. Adoption of high performance bipolar process enables the frequency division of 150MHz max.

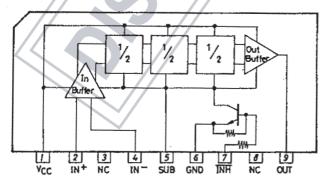
Features

- . FM frequency can be divided into C-MOS operating frequency.
- . Control pin is provided to inhibit the operation of prescaler when it is not in use.
- . If used in conjunction with our PLL synthesizer LC7220, electronic tuning system can be formed.

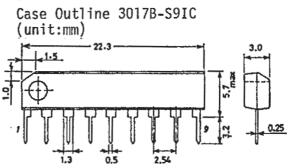
Absolute Maximum Ratings at Ta=2	5°C					
Maximum Supply Voltage	VCCmax		// 8	3	٧	
Allowable Power Dissipation	Pdmax	Ta=75°C	200) r	nW	
Operating Temperature	Topg		-20 to +80) '	°C	
Storage Temperature	T _{stg}	/-	40 to +125	, ,	,C	
Allowable Operating Condition at	Ta=25°C		min	typ	max	unit
Supply Voltage Range	VCC		4.5		5.5	v
Electrical Characteristics at Ta	=25°C		min	typ	max	unit
Current Dissipation	ICC			16	24	mA
Maximum Operating Frequency	fmax	vin=150mV _{rms}	150			MHz
Minimum Operating Frequency	fmin	$v_{in}=150mV_{rms}$			30	MHz
Input Voltage Range	VIN		100		600	mVrms
Output Amplitude	VouT		0.9			Vp-p

VINH(H)

Equivalent Circuit Block Diagram and Pin Assignment



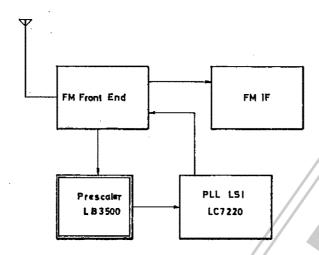
TNH Pin High Level Voltage



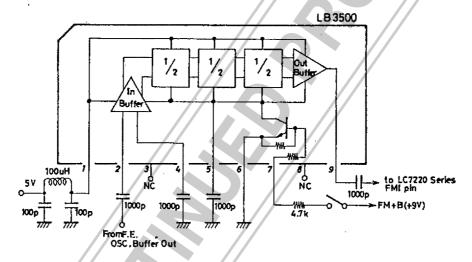
SANYO: SEP9

These specifications are subject to change without notice.

SANYO PLL Synthesizer Tuner System



Application Circuit



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