

PNP SILICON TRANSISTOR

DESCRIPTION

The 2SA733 is designed for use in diver stage of AF amplifier.

FEATURES

High hre and Excellent Linearity: 200 TYP.
 hre (Vce = -6.0 V, Ic = -1.0 mA)

ABSOLUTE MAXIMUM RATINGS

Maximum Temperature

Storage Temperature -55 to +150°C Junction Temperature +150°C Maximum

Maximum Power Dissipations ($T_A = 25^{\circ}C$)

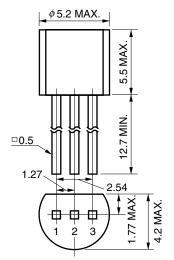
Total Power Dissipation 250 mW

Maximum Voltages and Currents ($T_A = 25^{\circ}C$)

 $\begin{array}{cccc} V_{\text{CBO}} & Collector \ to \ Base \ Voltage & -60 \ V \\ V_{\text{CEO}} & Collector \ to \ Emitter \ Voltage & -50 \ V \\ V_{\text{EBO}} & Emitter \ to \ Base \ Voltage & -5.0 \ V \\ Ic & Collector \ Current & -100 \ mA \\ I_{\text{B}} & Base \ Current & -20 \ mA \\ \end{array}$

Note Pulse Test PW \leq 350 μ s, Duty Cycle \leq 2%

* PACKAGE DRAWING (Unit: mm)



1: Emitter EIAJ: SC-43B 2: Collector JEDEC: TO-92 3: Base IEC: PA33

ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
DC Current Gain	hfe	$V_{CE} = -6.0 \text{ V}, \text{ Ic} = -1.0 \text{ mA}$	90	200	600	
Gain Bandwidth Product	f⊤	$V_{CE} = -6.0 \text{ V}, I_{E} = 10 \text{ mA}$		180		MHz
Output Capacitance	Cob	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1.0 \text{ MHz}$		4.5		pF
Collector Cutoff Current	Ісво	V _{CB} = -60 V, I _E = 0 A			-0.1	μΑ
Emitter Cutoff Current	ІЕВО	$V_{EB} = -5.0 \text{ V, Ic} = 0 \text{ A}$			-0.1	μΑ
Base to Emitter Voltage	VBE	$I_{CE} = -6.0 \text{ A}, I_{C} = -1.0 \text{ mA}$	-0.58	-0.62	-0.68	V
Collector Saturation Voltage	V _{CE(sat)}	Ic = -100 mA, I _B = -10 mA		-0.18	-0.3	V

CLASSIFICATION OF hfe

Rank	R	Q	Р	Е
Range	90 to 180	135 to 270	200 to 400	300 to 600

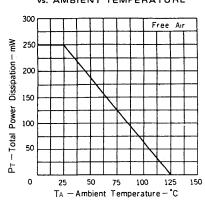
Remark here Test Conditions: $V_{CE} = -6.0 \text{ V}$, $I_{C} = -1.0 \text{ mA}$

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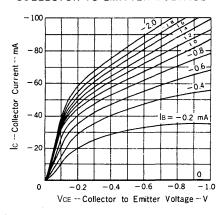


TYPICAL CHARACTERISTICS (TA = 25°C, otherwise noted.)

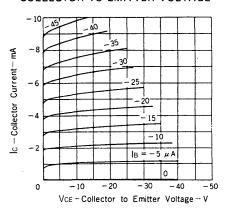
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



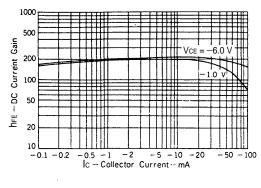
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



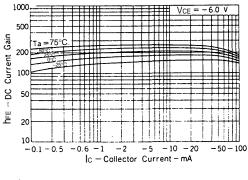
COLLECTOR CURRENT V5. COLLECTOR TO EMITTER VOLTAGE



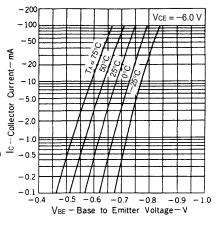
DC CURRENT GAIN vs. COLLECTOR CURRENT



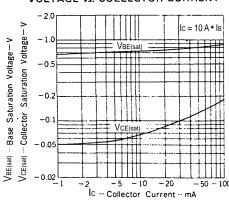
DC CURRENT GAIN vs. COLLECTOR CURRENT



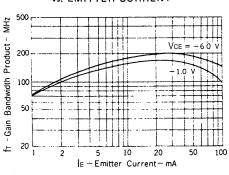
COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



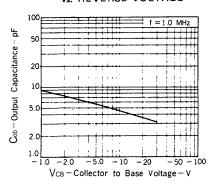
COLLECTOR AND BASE SATURATION VOLTAGE vs. COLLECTOR CURRENT



GAIN BANDWIDTH PRODUCT vs. EMITTER CURRENT

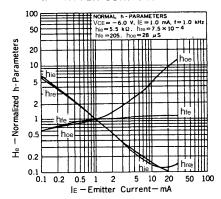


OUTPUT CAPACITANCE vs. REVERSE VOLTAGE

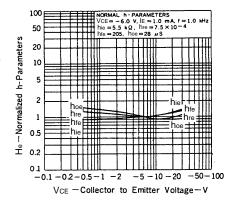




NORMALIZED h-PARAMETERS vs. EMITTER CURRENT



NORMALIZED h-PARAMETERS vs. COLLECTOR TO EMITTER VOLTAGE



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