

N-CHANNEL SILICON POWER MOS-FET

F-II SERIES

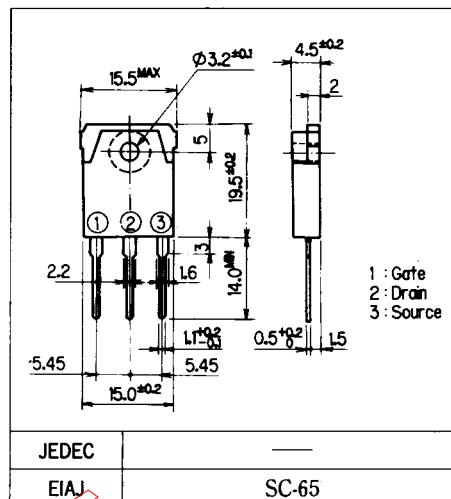
■ Features

- High speed switching
- Low on-resistance
- No secondary breakdown
- Low driving power
- High voltage
- $V_{GS} = \pm 30V$ Guarantee

■ Applications

- Switching regulators
- UPS
- DC-DC converters
- General purpose power amplifier

■ Outline Drawings

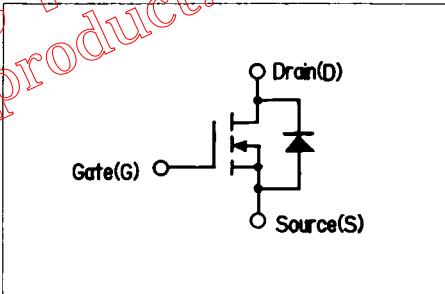


■ Max. Ratings and Characteristics

● Absolute Maximum Ratings($T_c = 25^\circ C$)

Items	Symbols	Ratings	Units
Drain-source voltage	V_{DSS}	800	V
Continuous drain current	I_D	9	A
Pulsed drain current	$I_D(\text{puls})$	26	A
Continuous reverse drain current	I_{DR}	9	A
Gate-source peak voltage	V_{GS}	± 30	V
Max. power dissipation	P_D	150	W
Operating and storage temperature range	T_{ch}	150	$^\circ C$
	T_{stg}	55 ~ +150	$^\circ C$

■ Equivalent Circuit Schematic

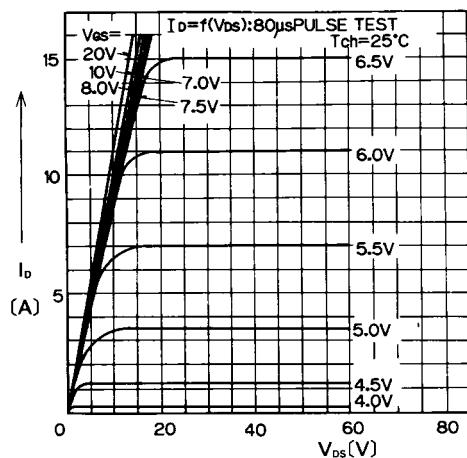
● Electrical Characteristics($T_c = 25^\circ C$)

Items	Symbols	Test Conditions	Min.	Typ.	Max.	Units
Drain-source breakdown voltage	$V_{(BR)DSS}$	$I_D = 1mA \quad V_{GS} = 0V$	800			V
Gate threshold voltage	$V_{GS(th)}$	$I_D = 1mA \quad V_{DS} = V_{GS}$	2.5	3.5	5.0	V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 800V \quad T_{ch} = 25^\circ C$ $V_{GS} = 0V \quad T_{ch} = 125^\circ C$	10	500	μA	
Gate-source leakage current	I_{GS}	$V_{GS} = \pm 30V \quad V_{DS} = 0V$	10	100	nA	
Drain-source on-state resistance	$R_{DS(on)}$	$I_D = 4A \quad V_{GS} = 10V$		1.0	1.5	Ω
Forward transconductance	g_{fs}	$I_D = 4A \quad V_{DS} = 25V$	3.0	6.0		S
Input capacitance	C_{iss}	$V_{DS} = 25V$		1400	2100	
Output capacitance	C_{oss}	$V_{GS} = 0V$		200	300	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz		110	160	
Turn-on time t_{on} ($t_{on} + t_{d(on)} + t_r$)	$t_{d(on)}$	$V_{CC} = 600V \quad I_D = 9A$	50	75		
	t_r		230	350		ns
Turn-off time t_{off} ($t_{d(off)} + t_f$)	$t_{d(off)}$	$V_{GS} = 10V$	300	450		
	t_f	$R_G = 25\Omega$	160	240		
Diode forward on-voltage	V_{SD}	$I_F = 2 \times I_{DR} \quad V_{GS} = 0V \quad T_{ch} = 25^\circ C$		1.05	1.58	V
Reverse recovery time	t_{rr}	$I_F = I_{DR} \quad d_i/dt = 100A/\mu s \quad T_{ch} = 25^\circ C$		1000		ns

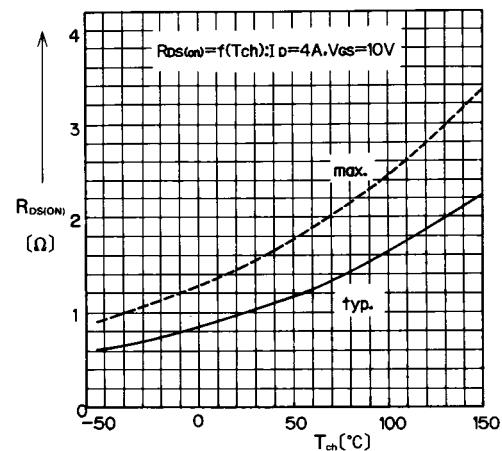
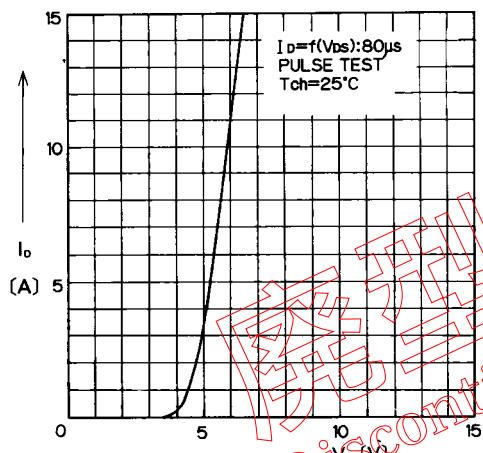
● Thermal Characteristics

Items	Symbols	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance	$R_{th(ch-a)}$	channel to air			35.0	$^\circ C/W$
	$R_{th(ch-c)}$	channel to case			0.83	$^\circ C/W$

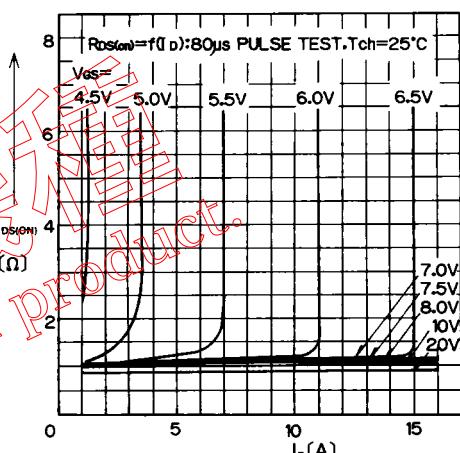
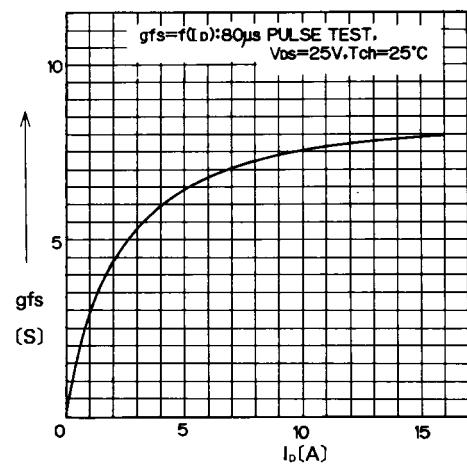
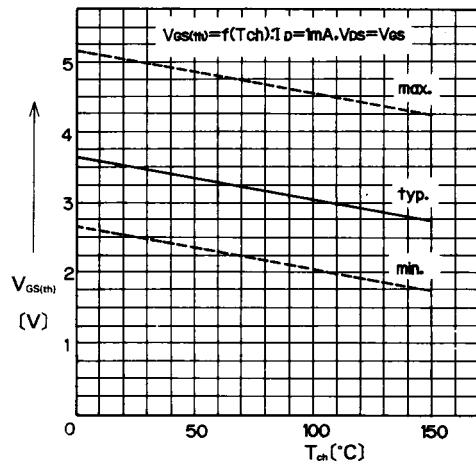
■ Characteristics

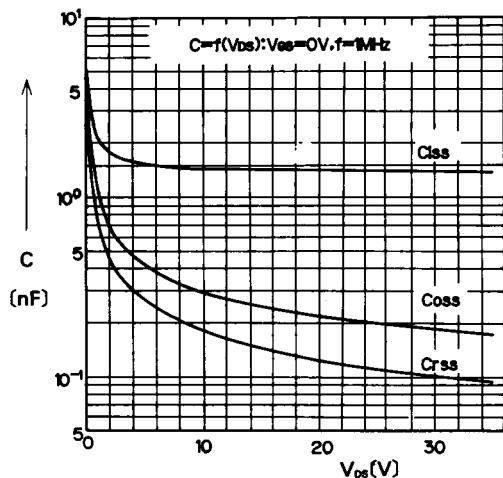
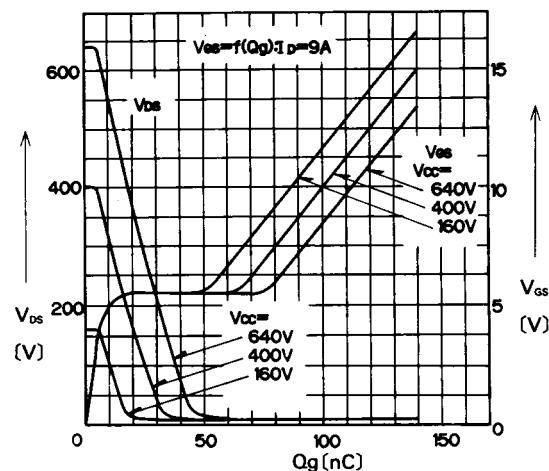


Typical Output Characteristics

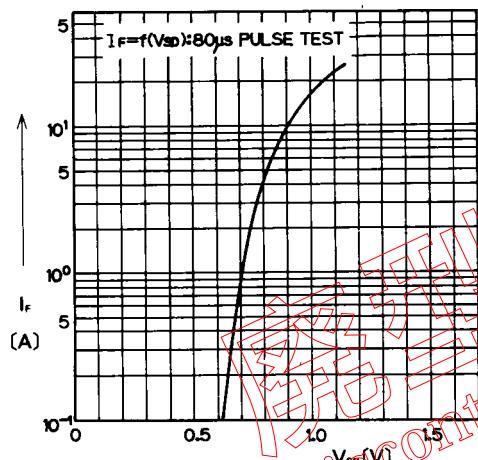
On State Resistance vs. T_{ch} 

Typical Transfer Characteristics

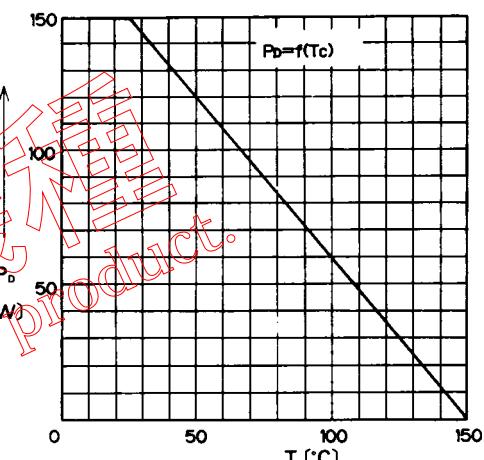
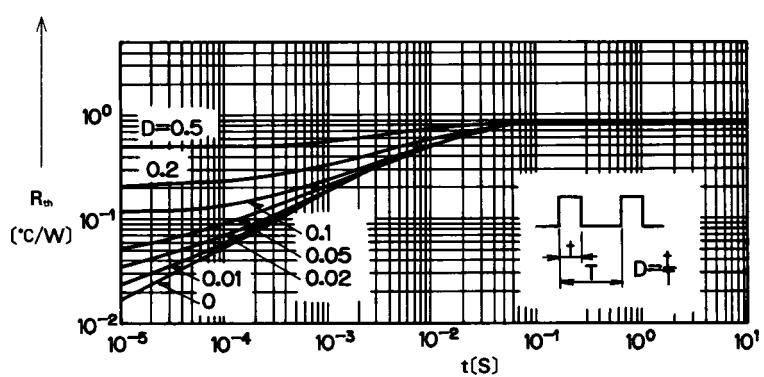
Typical Drain-Source on State Resistance vs. I_D Typical Forward Transconductance vs. I_D Gate Threshold Voltage vs. T_{ch}

Typical Capacitance vs. V_{DS} 

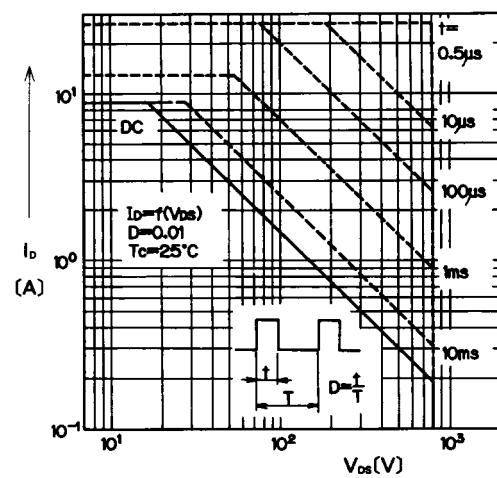
Typical Input Charge



Forward Characteristics of Reverse Diode

Allowable Power Dissipation vs. T_c 

Transient Thermal Impedance



Safe Operating Area