NPN Triple Diffused Planar Silicon Transistor



TT2138LS

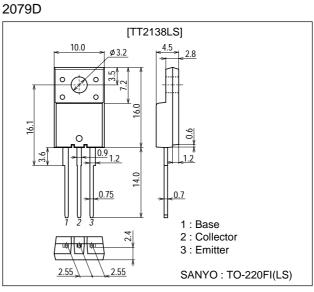
Color TV Horizontal Deflection Output Applications

Features

- High speed.
- High breakdown voltage (VCBO=1500V).
- High reliability (Adoption of HVP process).
- · Adoption of MBIT process.
- · On-chip damper diode.

Package Dimensions

unit : mm



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		1500	V
Collector-to-Emitter Voltage	VCEO		800	V
Emitter-to-Base Voltage	VEBO		5	V
Collector Current	IC		3.5	А
Collector Current (Pulse)	ICP		9	А
Collector Dissipation	PC		2.0	W
		Tc=25°C	25	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector Cutoff Current	ICBO	V _{CB} =800V, I _E =0			10	μΑ
Collector Cutoff Current	ICES	VCE=1500V, RBE=0			1.0	mA
Collector Sustain Voltage	VCEO(sus)	IC=100mA, IB=0	800			V
Emitter Cutoff Current	IEBO	V _{EB} =4V, I _C =0	40		130	mA

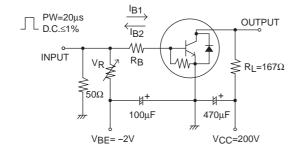
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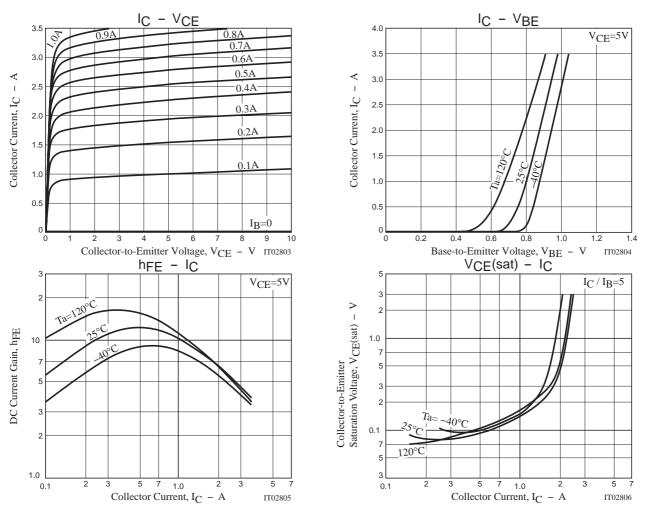
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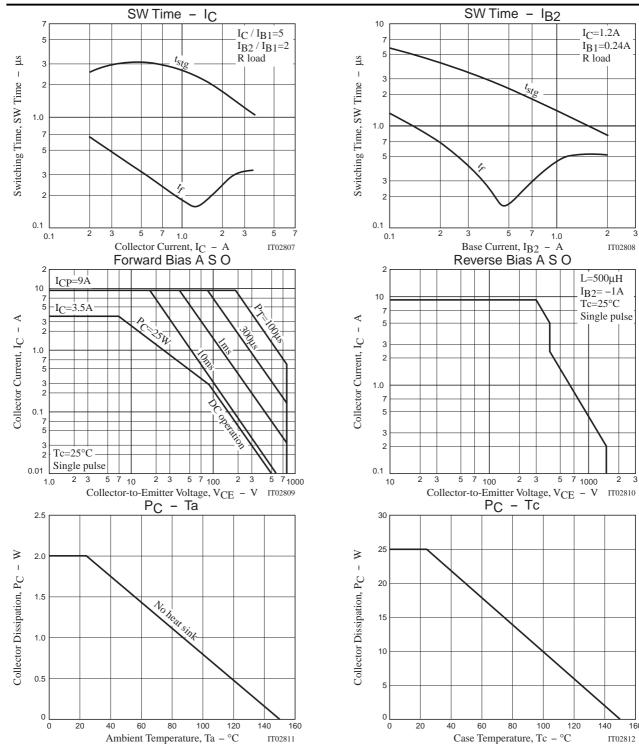
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Onit
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	IC=1.8A, IB=0.36A			3	V
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	IC=1.8A, IB=0.36A			1.5	V
DC Current Gain	hFE1	VCE=5V, IC=0.5A	5			
	hFE2	V _{CE} =5V, I _C =2A	5		8	
Diode Forward Voltage	VF	IEC=3A			2	V
Fall Time	tf	IC=1.2A, IB1=0.24A, IB2=-0.48A			0.3	μs

Switching Time Test Circuit





No.7214-2/4



 $I_C=1.2A$

tstg

5

1.0

100

120

140

160

IT02812

80

I_{B1}=0.24A R load

2

L=500µH

 $I_{B2} = -1A$ Tc=25°C

Single pulse

3

IT02808

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