TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

## 2SK2843

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

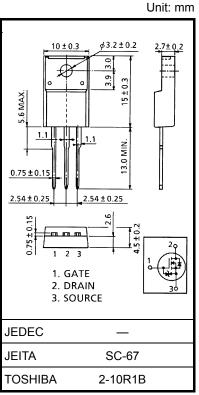
• Low drain-source ON resistance :  $R_{DS (ON)} = 0.54 \Omega (typ.)$ 

High forward transfer admittance : |Y<sub>fs</sub>| = 9.0 S (typ.)
 Low leakage current : I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 600 V)

• Enhancement mode :  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	600	V
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	$V_{DGR}$	600	V
Gate-source voltage		V <sub>GSS</sub>	±30	V
Drain current	DC (Note 1)	ID	10	Α
	Pulse (Note 1)	I <sub>DP</sub>	40	Α
Drain power dissipatio	n (Tc = 25°C)	P <sub>D</sub>	45	W
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	363	mJ
Avalanche current		I <sub>AR</sub>	10	Α
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	5.0	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature r	ange	T <sub>stg</sub>	-55 to 150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 6.36 mH,  $R_{G}$  = 25  $\Omega$ ,  $I_{AR}$  = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

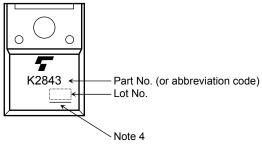
#### **Electrical Characteristics (Ta = 25°C)**

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cur	rent	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_	_	V
Gate threshold v	roltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source OI	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A	_	0.54	0.75	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5 A	3.0	9.0	_	S
Input capacitano	e	C <sub>iss</sub>		_	2040	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	230	_	pF
Output capacitance		Coss			590	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{DD}$ $V_{DD}$ $V_{DD}$ $V_{DD}$	_	22	_	- ns
	Turn-on time	t <sub>on</sub>		_	58	_	
	Fall time	t <sub>f</sub>			36		
	Turn-off time	t <sub>off</sub>			190	_	
Total gate charge (gate-source plus gate-drain)		Qg			45	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		25	_	nC
Gate-drain ("miller") Charge		Q <sub>gd</sub>			20	_	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	10	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	40	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V		1300		ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 A / μs		16	1	μC

#### Marking

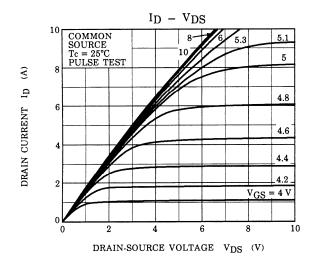


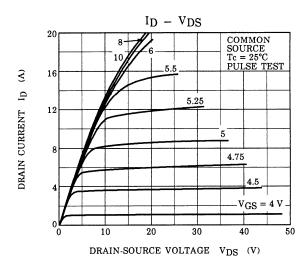
Note 4: A line under a Lot No. identifies the indication of product Labels.

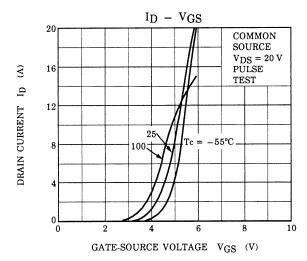
Not underlined: [[Pb]]/INCLUDES > MCV

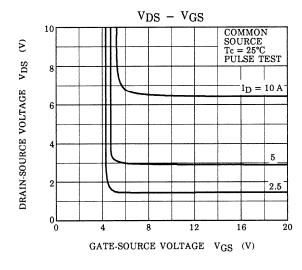
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

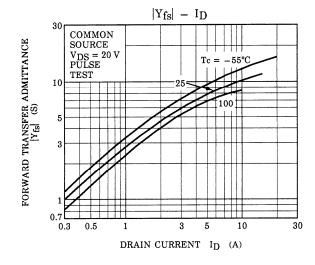
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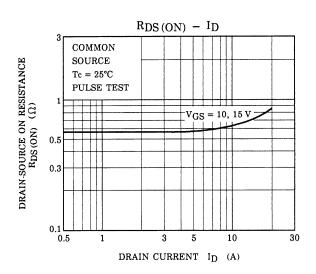




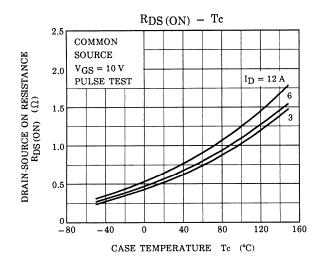


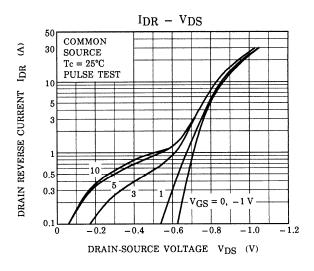


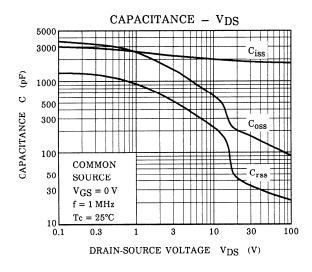


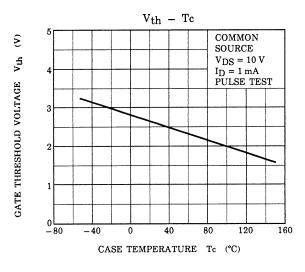


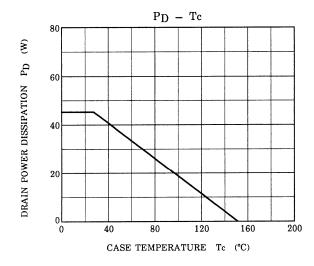
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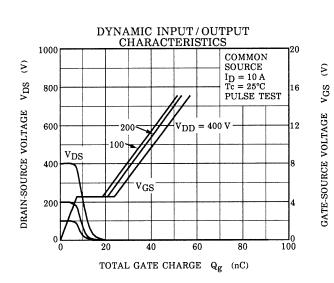


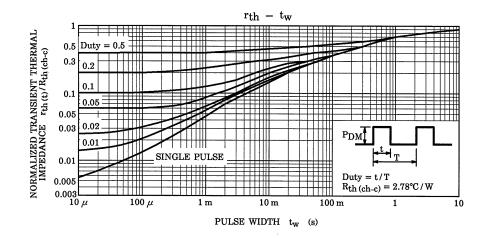


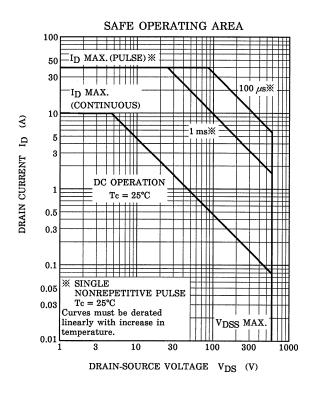


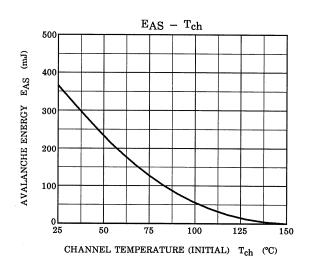


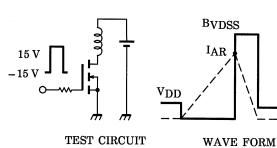












$$R_G$$
 = 25  $\Omega$   
 $V_{DD}$  = 90 V, L = 6.36 mH

$$EAS = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

 $v_{DS}$ 

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