Silicon NPN Epitaxial

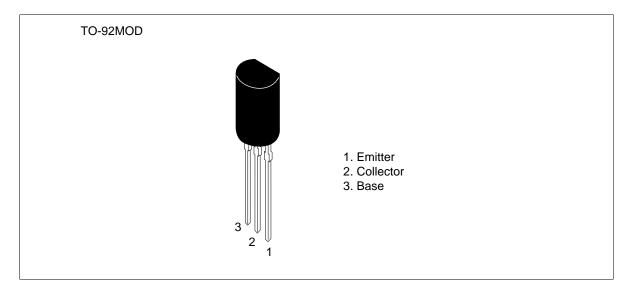
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ADE-208-1140A (Z) 2nd. Edition Mar. 2001

Application

- Low frequency power amplifier
- Complementary pair with 2SB740

Outline



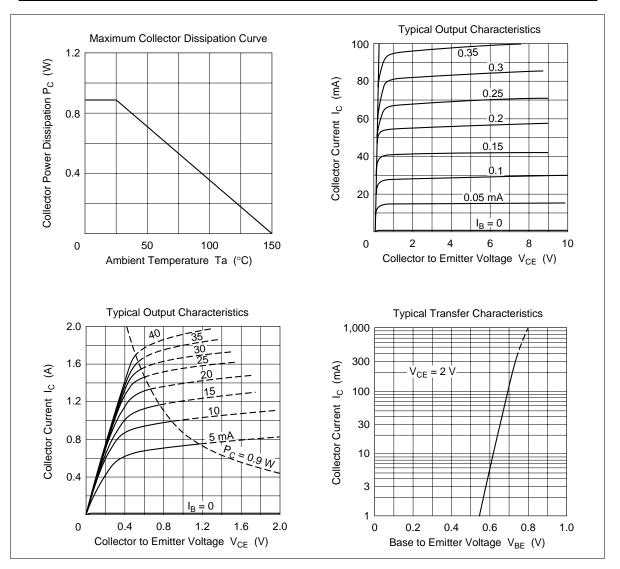


Absolute Maximum Ratings (Ta = 25° C)

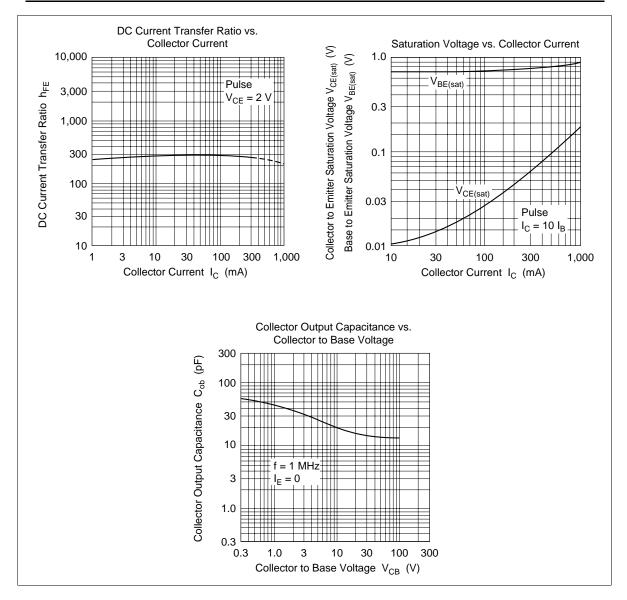
Item	Symbol	Ratings	Unit
Collector to base voltage	V _{CBO}	100	V
Collector to emitter voltage	V _{CEO}	50	V
Emitter to base voltage	V _{EBO}	6	V
Collector current	I _c	1	А
Collector power dissipation	Pc	0.9	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Electrical Characteristics (Ta = 25° C)

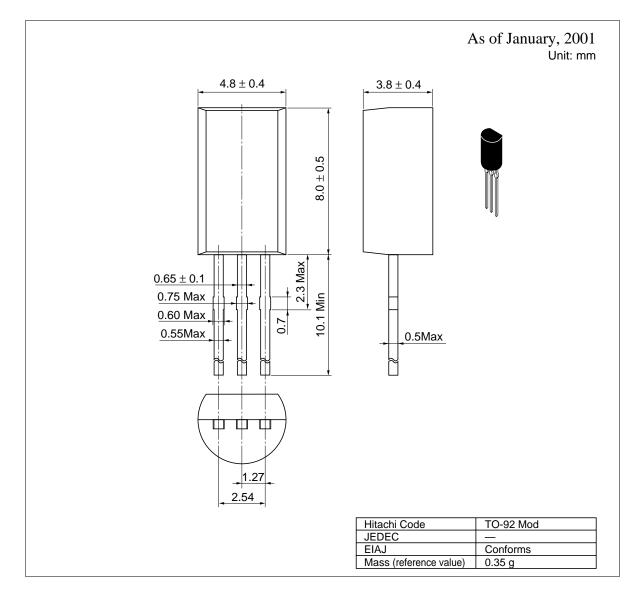
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{\rm (BR)CBO}$	100	_	_	V	$I_{c} = 10 \ \mu A, \ I_{E} = 0$
Collector to emitter breakdown voltage	$V_{(\text{BR})\text{CEO}}$	50	—	_	V	$I_c = 1 \text{ mA}, R_{BE} = \infty$
Emitter to base breakdown voltage	$V_{(\text{BR})\text{EBO}}$	6	—	—	V	$I_{\rm E} = 10 \ \mu A, \ I_{\rm C} = 0$
Collector cutoff current	I _{CBO}		_	1	μA	$V_{CB} = 80 \text{ V}, I_{E} = 0$
Emitter cutoff current	I _{EBO}	—	—	0.2	μA	$V_{EB} = 6 V, I_{C} = 0$
DC current transfer ratio	h_{FE}^{*1}	100	—	800		$V_{ce} = 2 V, I_c = 0.1A$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$	_	—	0.3	V	$I_{\rm c} = 1 \text{ A}, I_{\rm B} = 0.1 \text{ A}$
Gain bandwidth product	f _⊤	_	100		MHz	$V_{ce} = 2 V, I_c = 10 mA$
Collector output capacitance	Cob	—	20		pF	$V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1 \text{MHz}$
Note: 1. The 2SD789 is grou	uped by h _{FE}	as follo	WS.			
B C D		Е				
100 to 200 160 to 320 2	50 to 500	400 to	800			



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Package Dimensions



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